



The Magnificent Seven Challenges & Opportunities in Domain-Specific Accelerator Design for Autonomous Systems



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Robotics is a **Big** and **Embodied** space

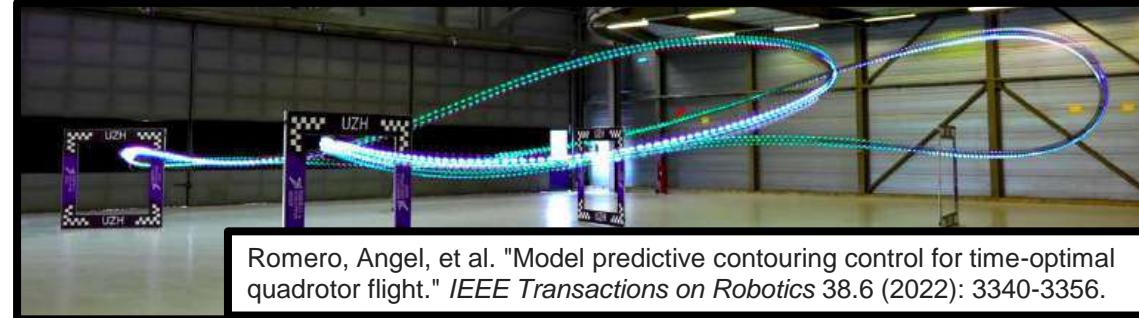


Autonomous Systems

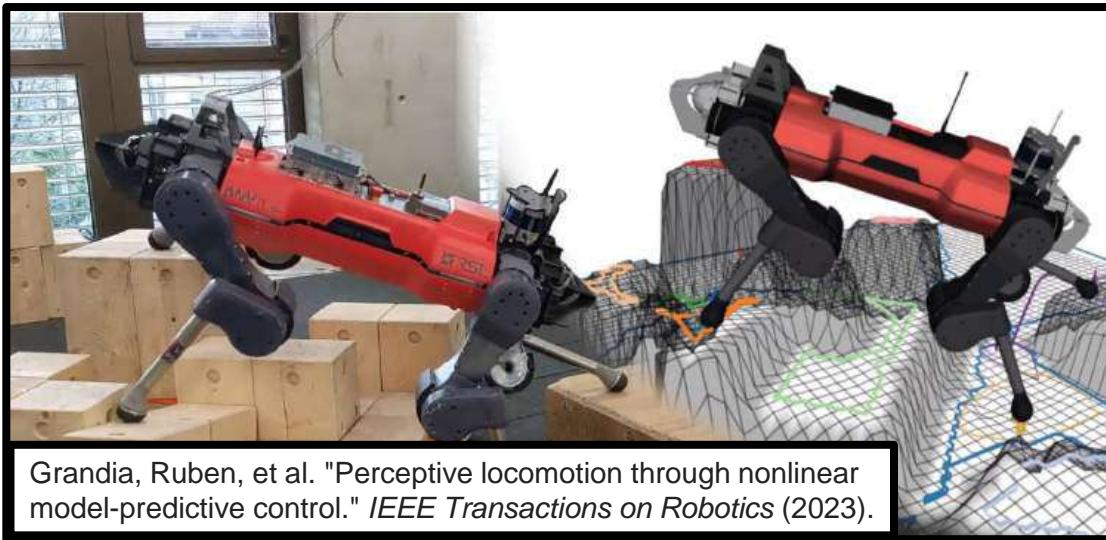
~~Robotics~~ is a **Big** and Embodied space



Autonomous Systems can do amazing things ...

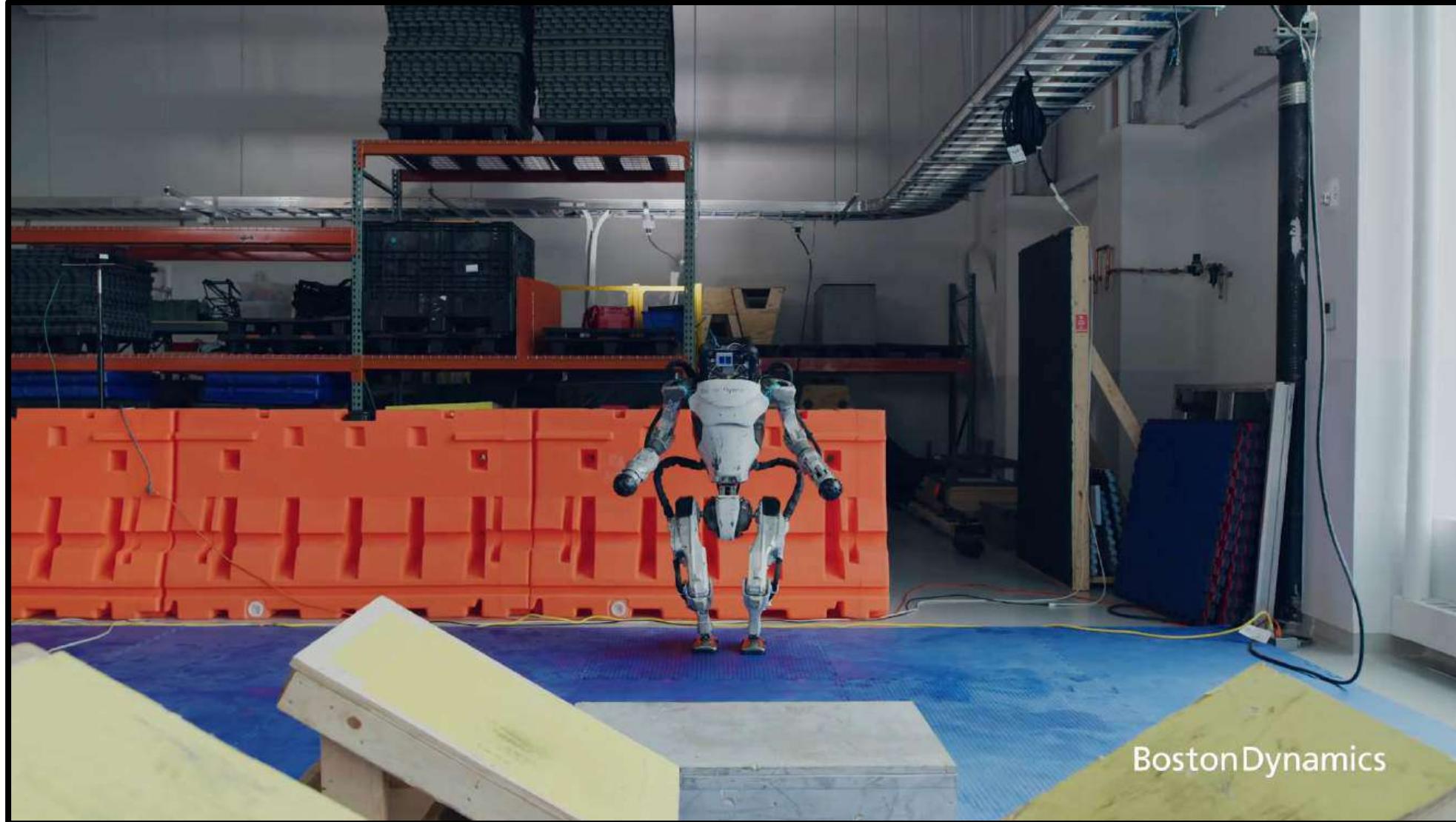


Romero, Angel, et al. "Model predictive contouring control for time-optimal quadrotor flight." *IEEE Transactions on Robotics* 38.6 (2022): 3340-3356.



Grandia, Ruben, et al. "Perceptive locomotion through nonlinear model-predictive control." *IEEE Transactions on Robotics* (2023).

Autonomous Systems can do amazing things ...



... but they still have a long way to go!



... but they still have a long way to go!



Grandia, Ruben, et al. "Perceptive locomotion model-predictive control." *IEEE Transaction*



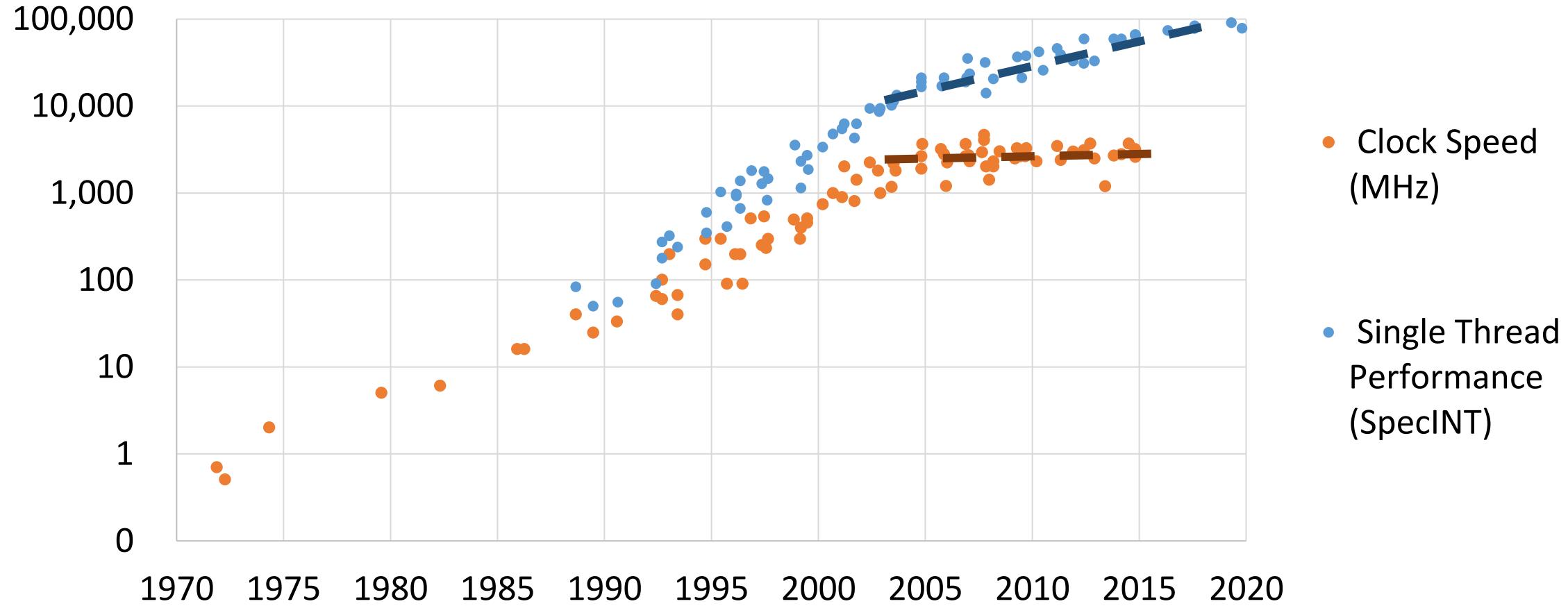
Inside the lab: How does Atlas work?
([youtube.com/watch?v=EezdinoG4mk](https://www.youtube.com/watch?v=EezdinoG4mk))



Unfortunately, we have reached the limits of technology scaling and on-chip power density...



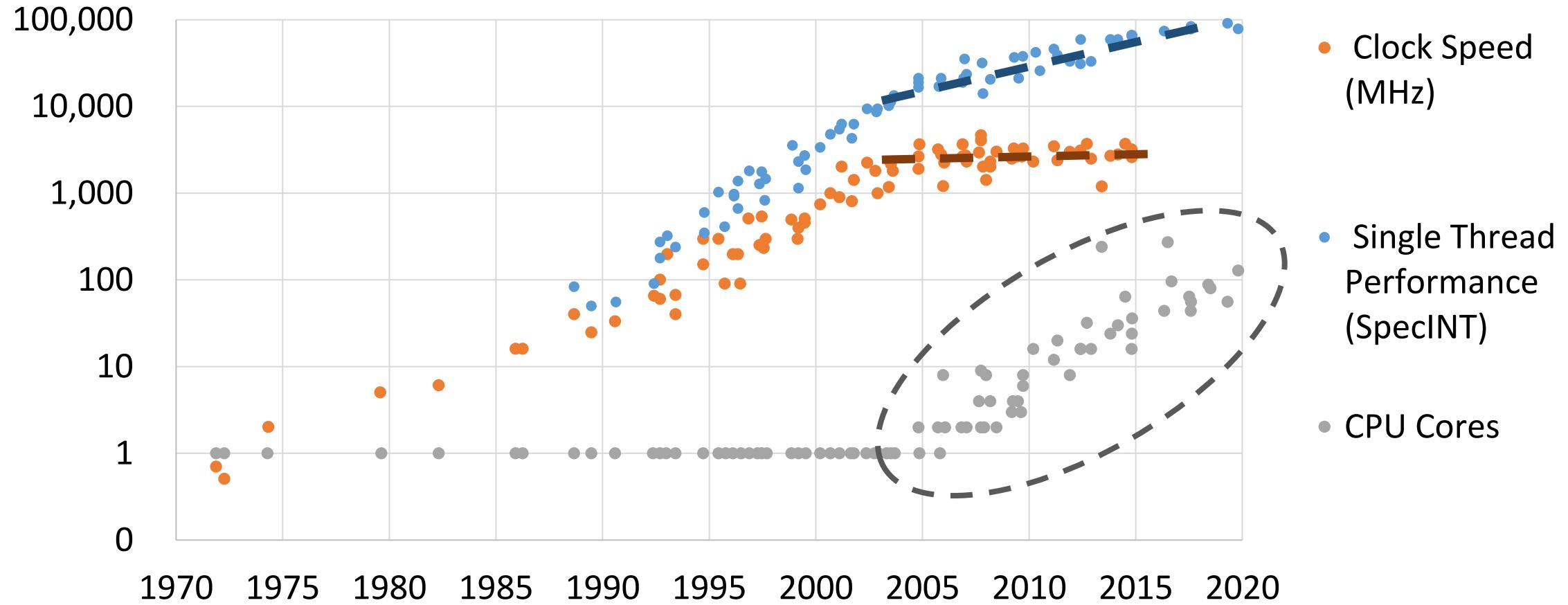
48 Years of Processor Trends



Original data up to the year 2010 collected and plotted by M. Horowitz, F. Labonte, O. Shacham, K. Olukotun, L. Hammond, and C. Batten
New plot and data collected for 2010-2019 by K. Rupp <https://github.com/karlrupp/microprocessor-trend-data>

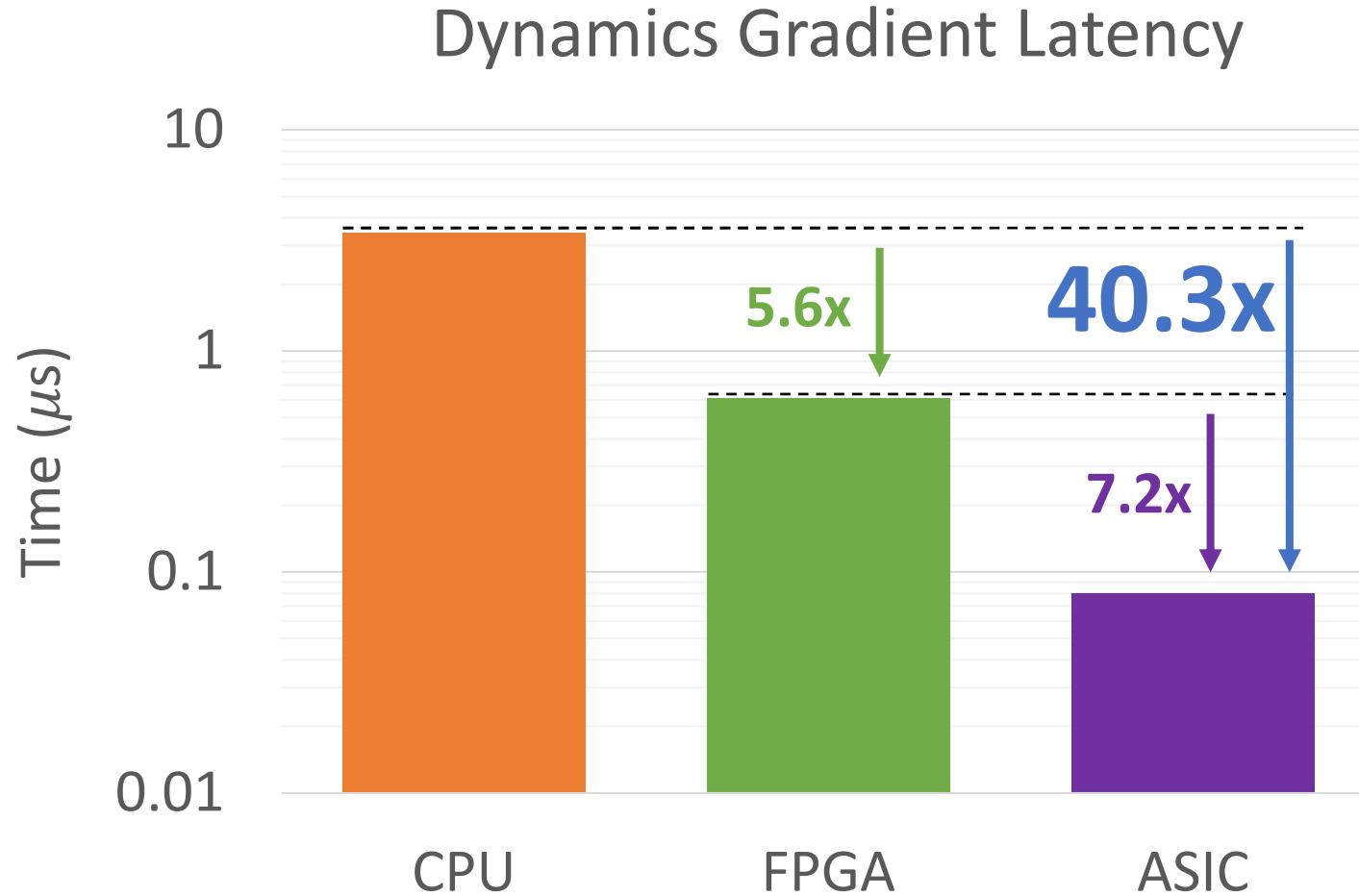
...resulting in a need to leverage parallelism...

48 Years of Processor Trends

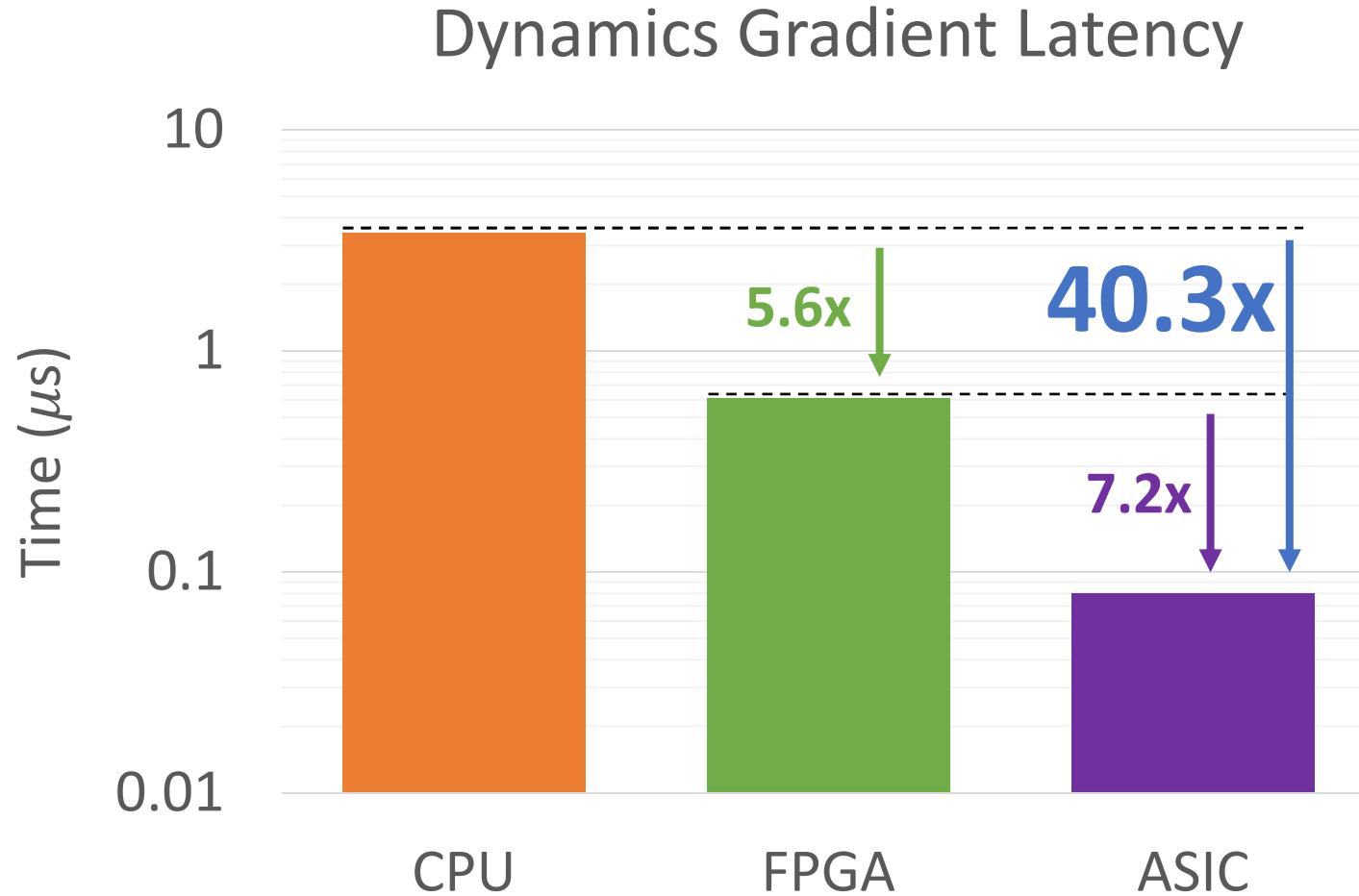


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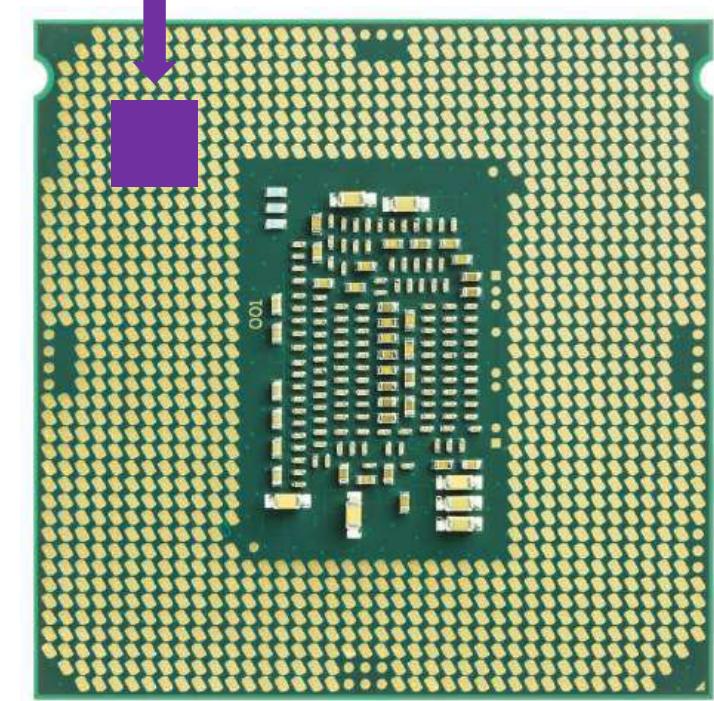
...and move toward specialized accelerators!



...and move toward specialized accelerators!

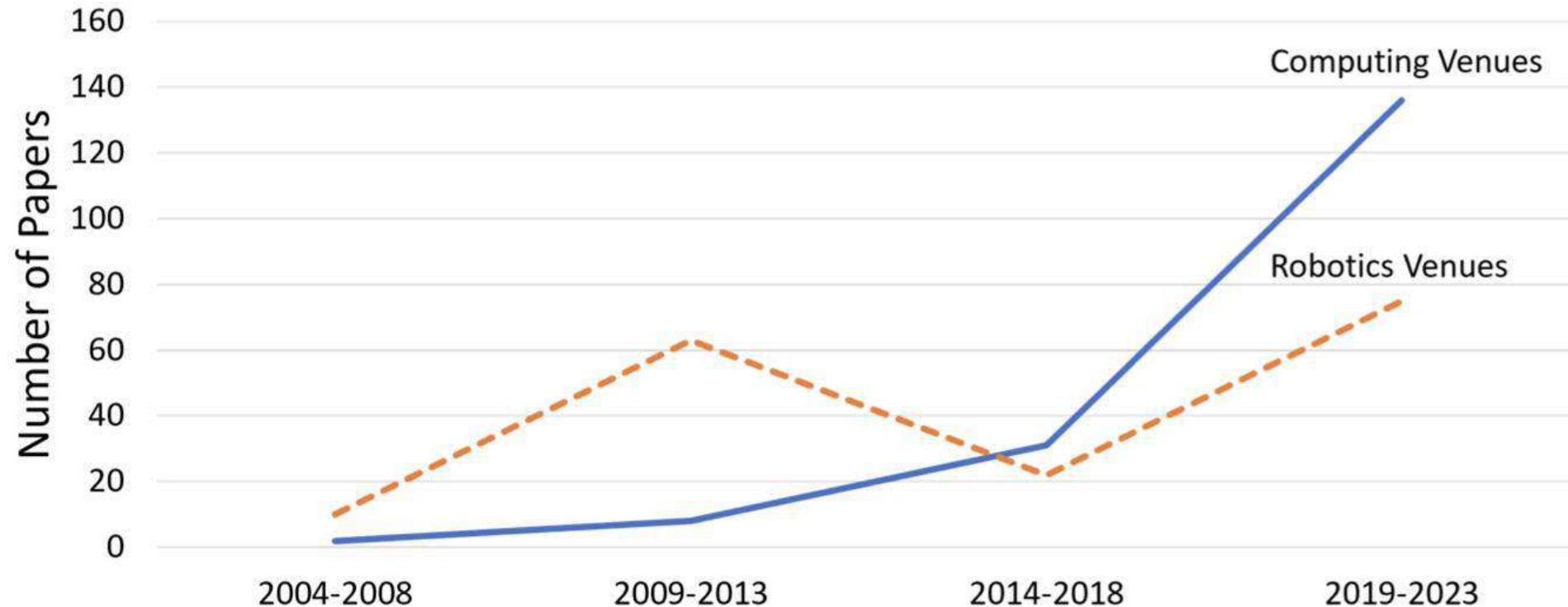


65x smaller than
a standard CPU





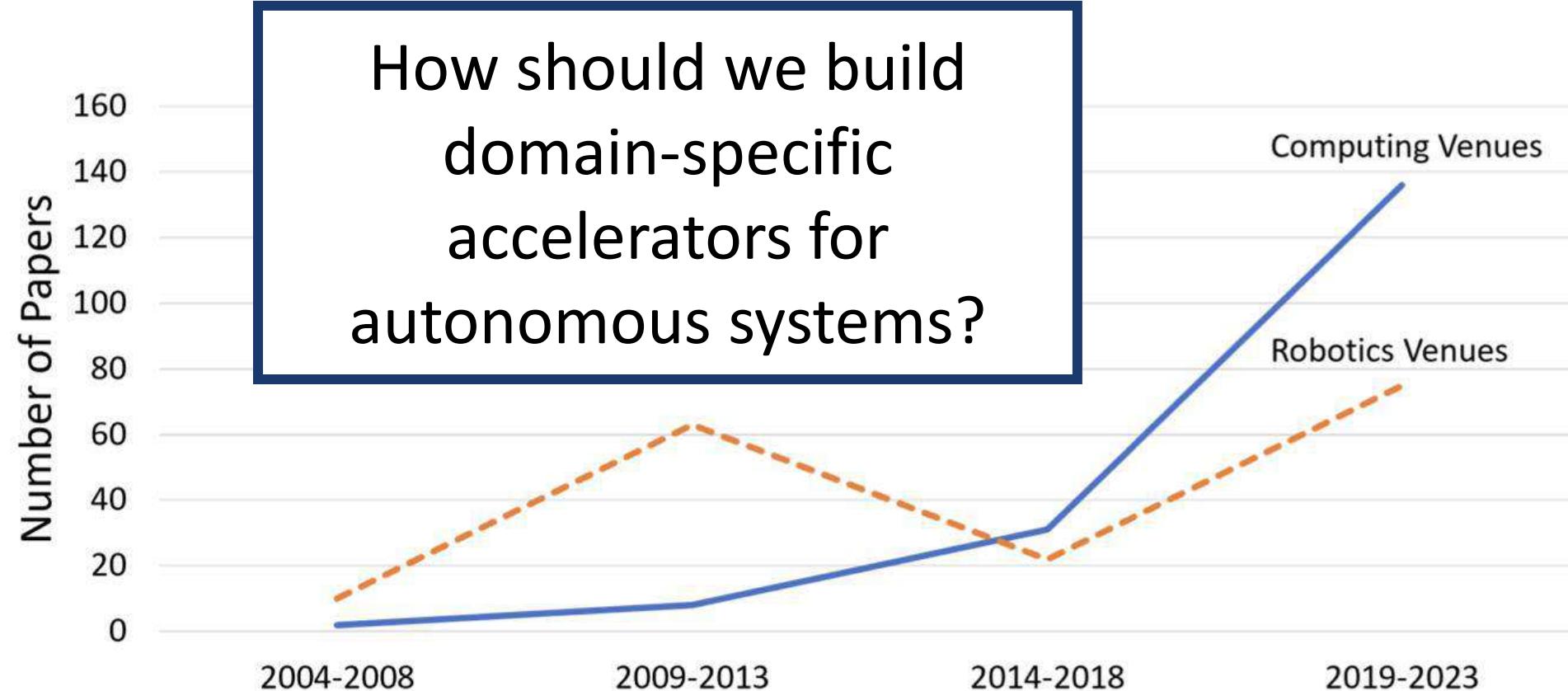
Interest in Accelerators for Autonomous Systems is Growing!



Computing Keywords: Accelerator & Robot | Autonomous System | UAV | Drone | Autonomous Vehicle | Self-Driving, in DAC, ISCA, MICRO, HPCA, and ASPLOS. Robotics Keywords: ASIC | FPGA, in ICRA, IROS, RSS, and RA-L.



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The Magnificent Seven

Challenges & Opportunities in Domain-Specific Accelerator Design for Autonomous Systems



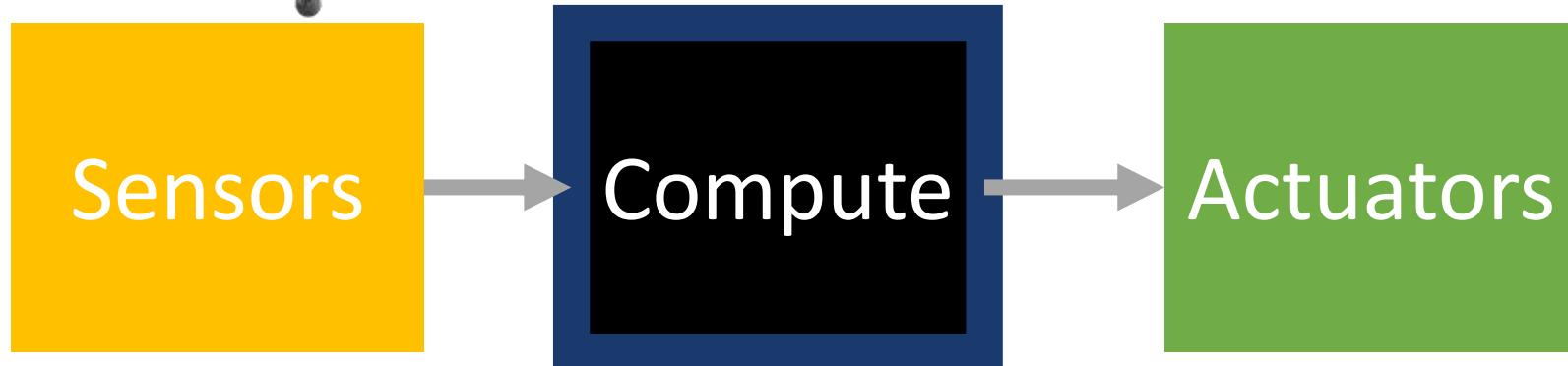
- ① Build Bridges: Engage with Domain Experts
- ② Measure Twice, Cut Once: Metrics Matter
- ③ “Widgetism”: Avoid Over-Specialization
- ④ Pump the Brakes: Do Not Always Accelerate
- ⑤ Chips and Salsa: Acceleration Beyond ASICs
- ⑥ Forest vs. Trees: Take an End-to-End View
- ⑦ Design Global: Sustainability and Impact



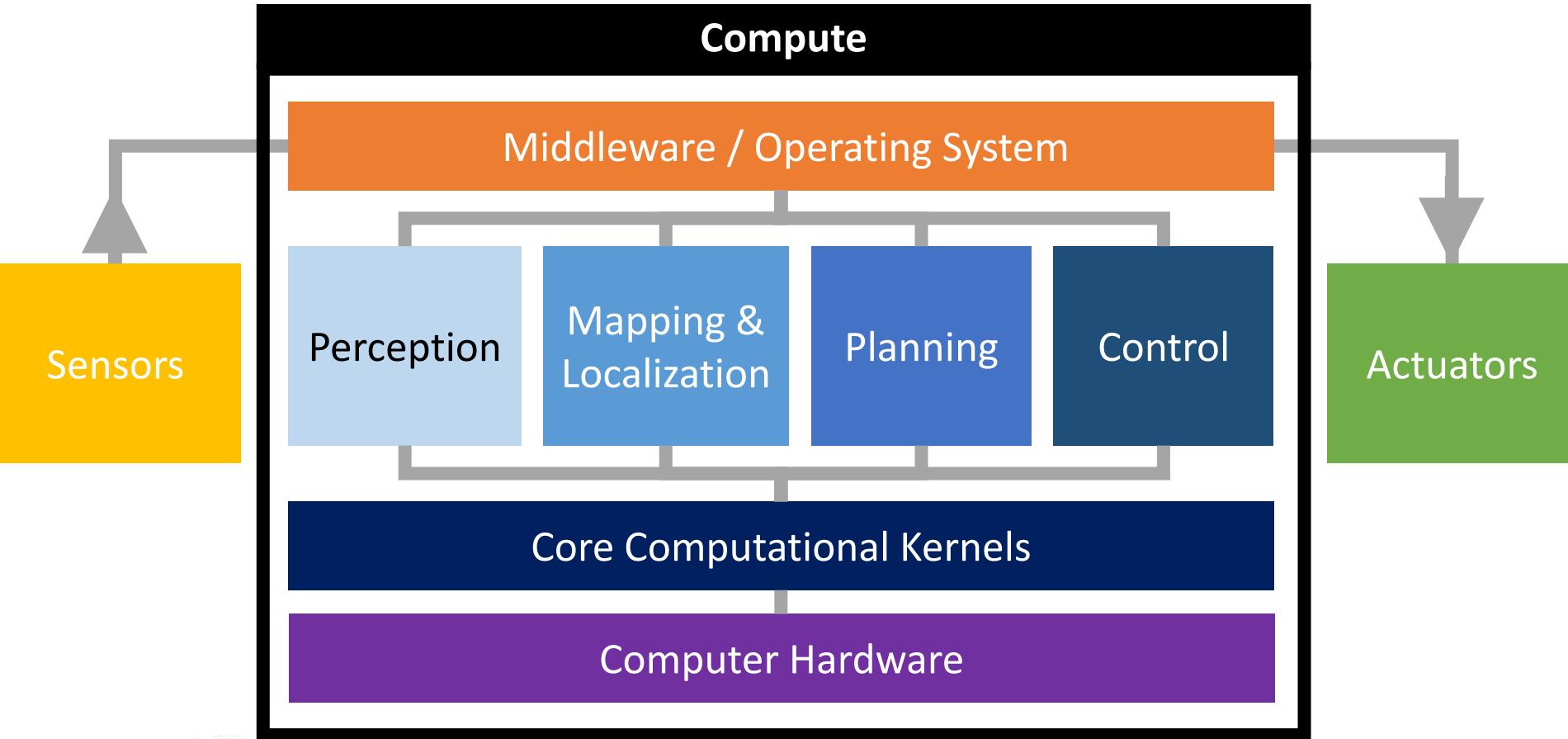
1 Build Bridges: Engage with Domain Experts

Pitfall: *Interact with domains exclusively through benchmarks published in computer systems, without input from domain experts.*

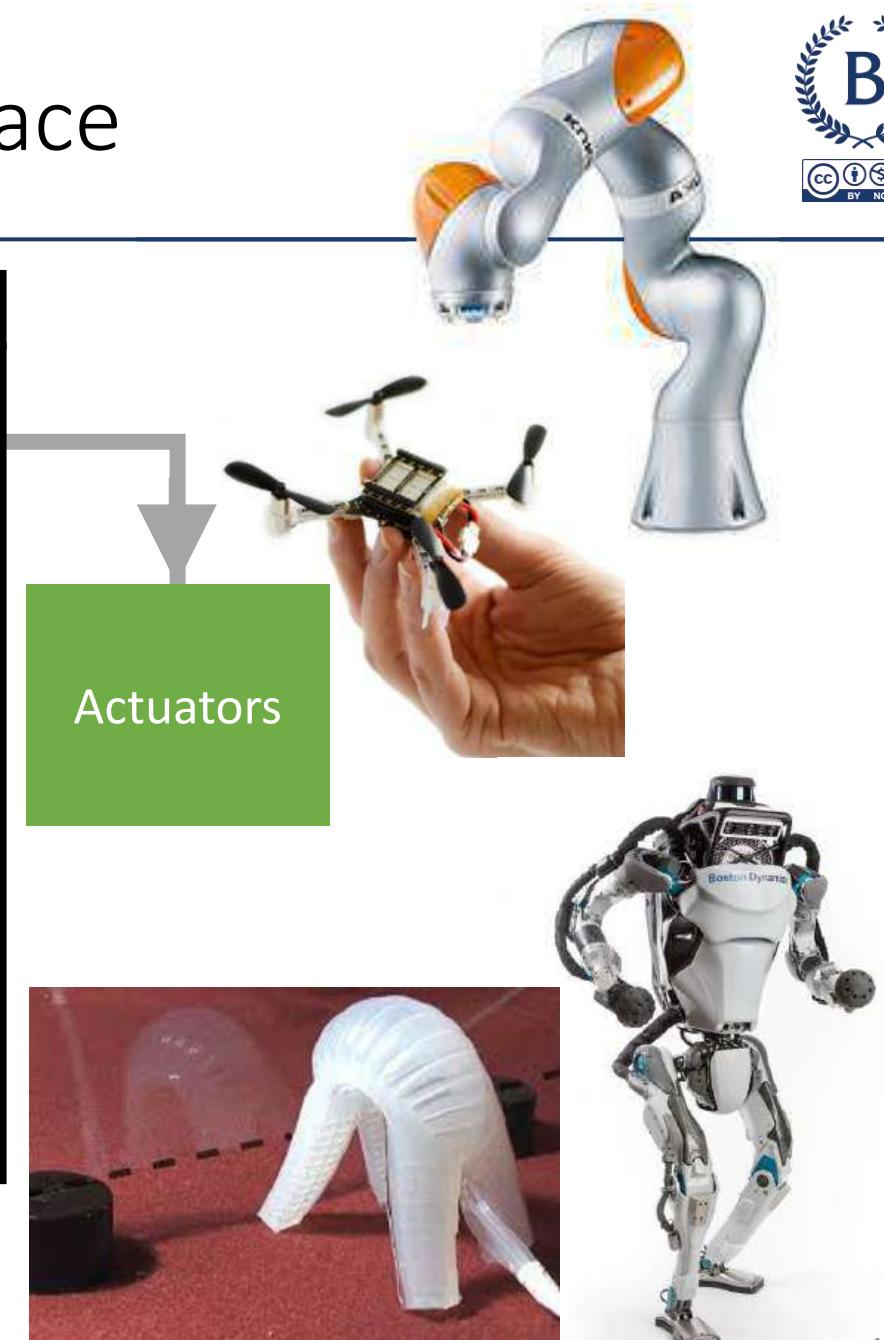
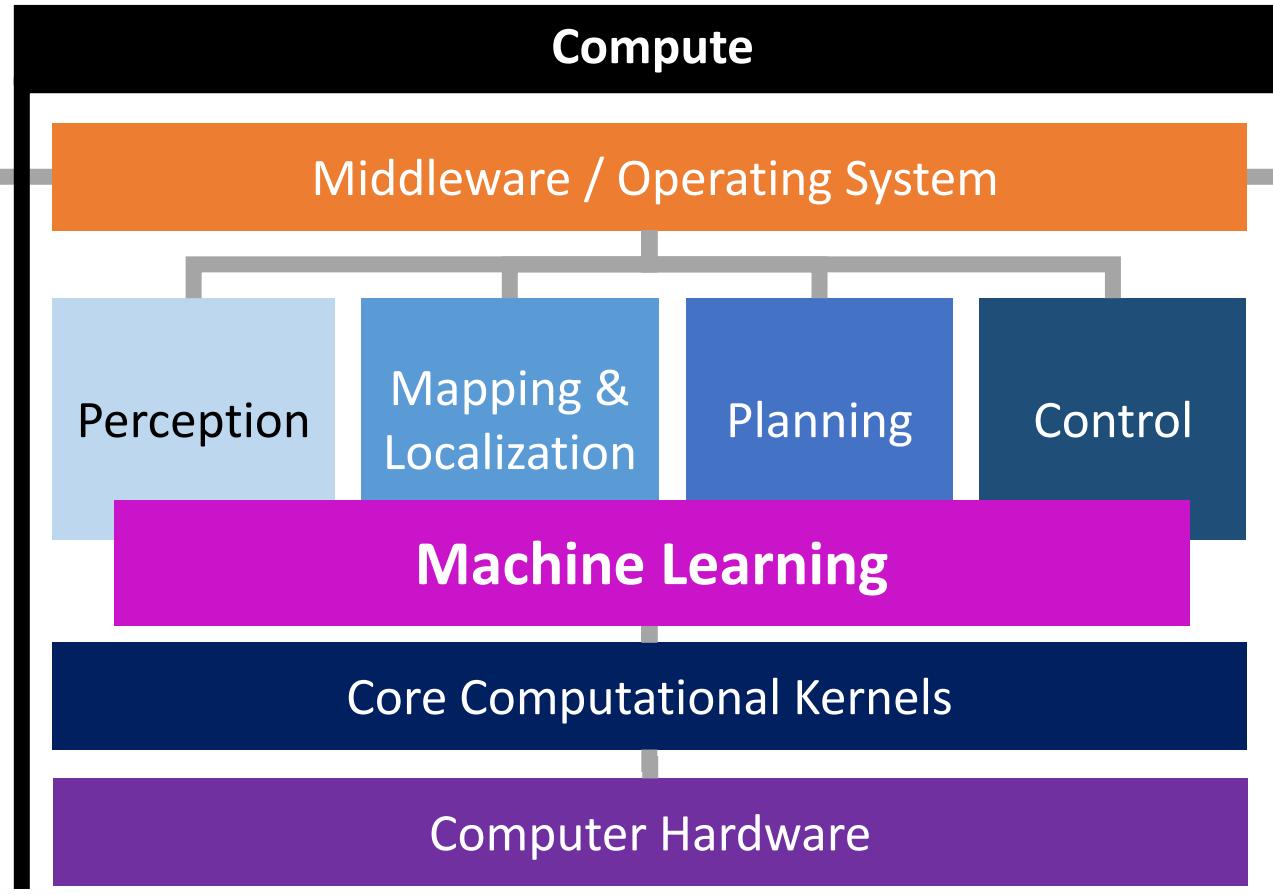
1 Robotics is a **Big** and **Embodied** space



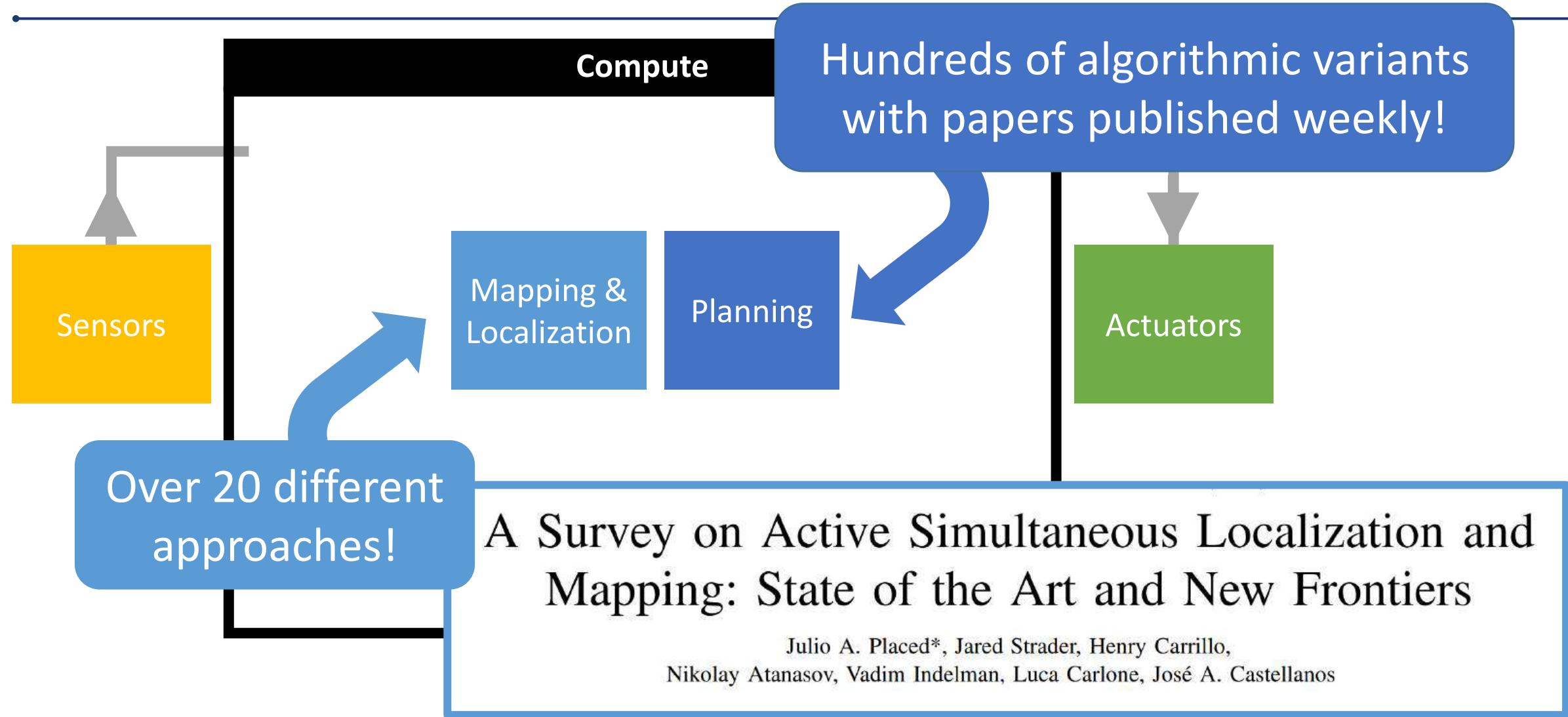
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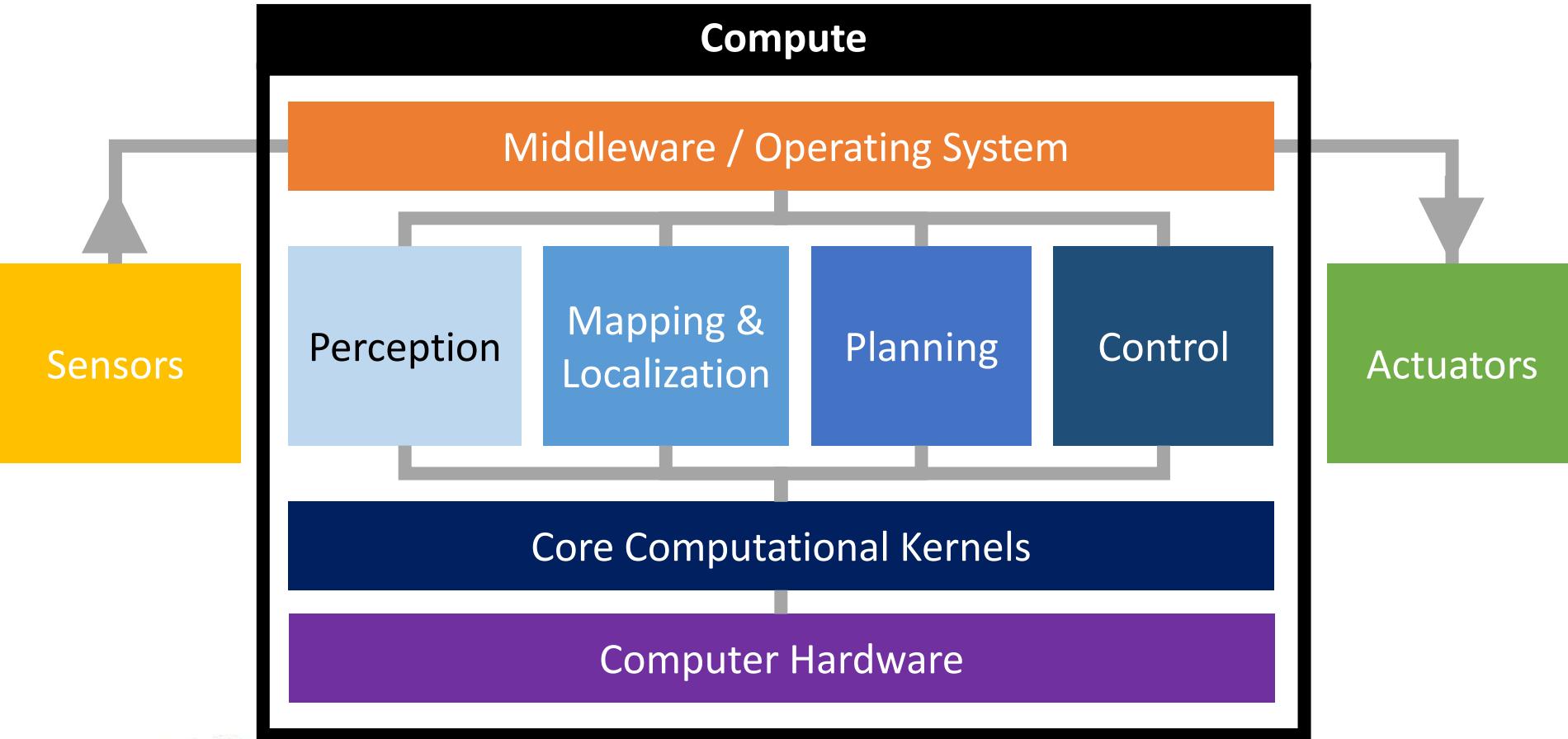
1 Robotics is a **Big** and **Embodied** space



1 Robotics is a **Big** and **Embodied** space



1 Build Bridges: Engage with Domain Experts





③ “Widgetism”: Avoid Over-Specialization

Pitfall: *A cycle of pick one slow algorithm, lower it to an ASIC, repeat.*



5 Chips and Salsa: Acceleration Beyond ASICs

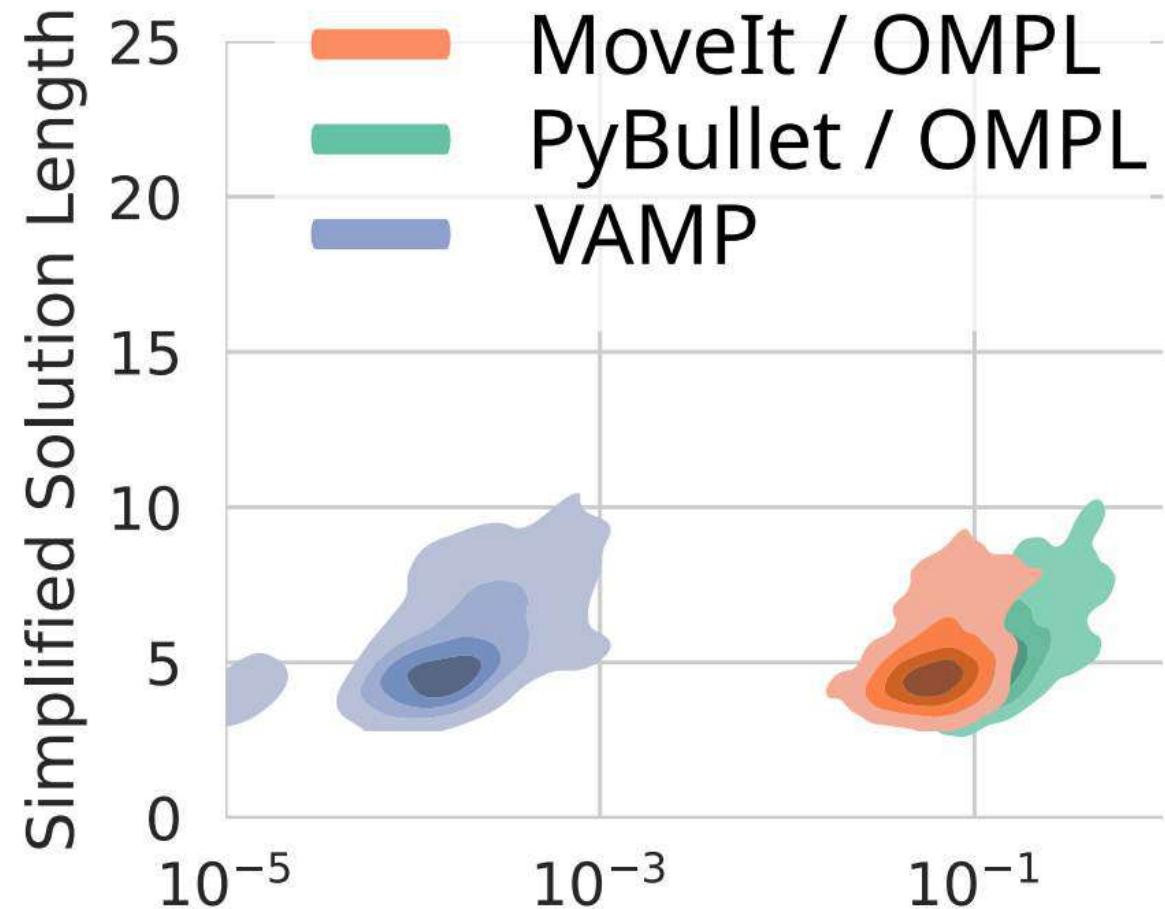
Pitfall: Focus on ASICs, leaving software, GPUs, and FPGAs behind.

“Widgetism”: Avoid Over-Specialization

Chips and Salsa: Acceleration Beyond ASICs



Software Performance
Engineering can get you
pretty far!

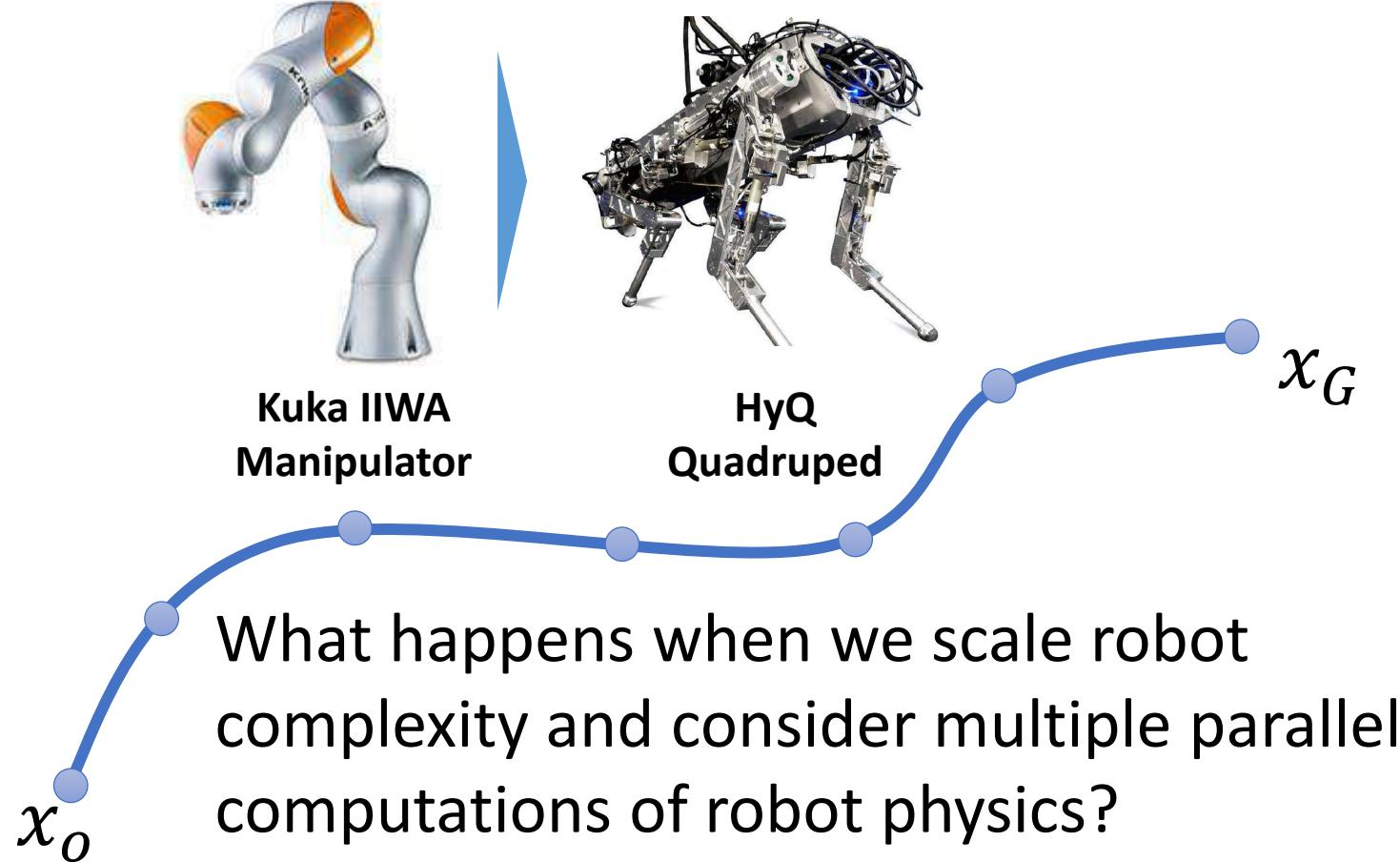


“Widgetism”: Avoid Over-Specialization

Chips and Salsa: Acceleration Beyond ASICs



GPUs give you the flexibility to quickly iterate plus improved performance!

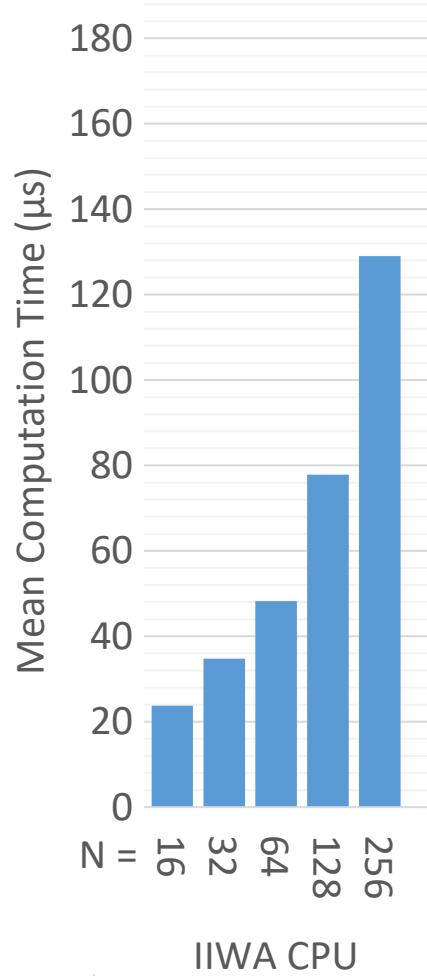


3,5

“Widgetism”: Avoid Over-Specialization

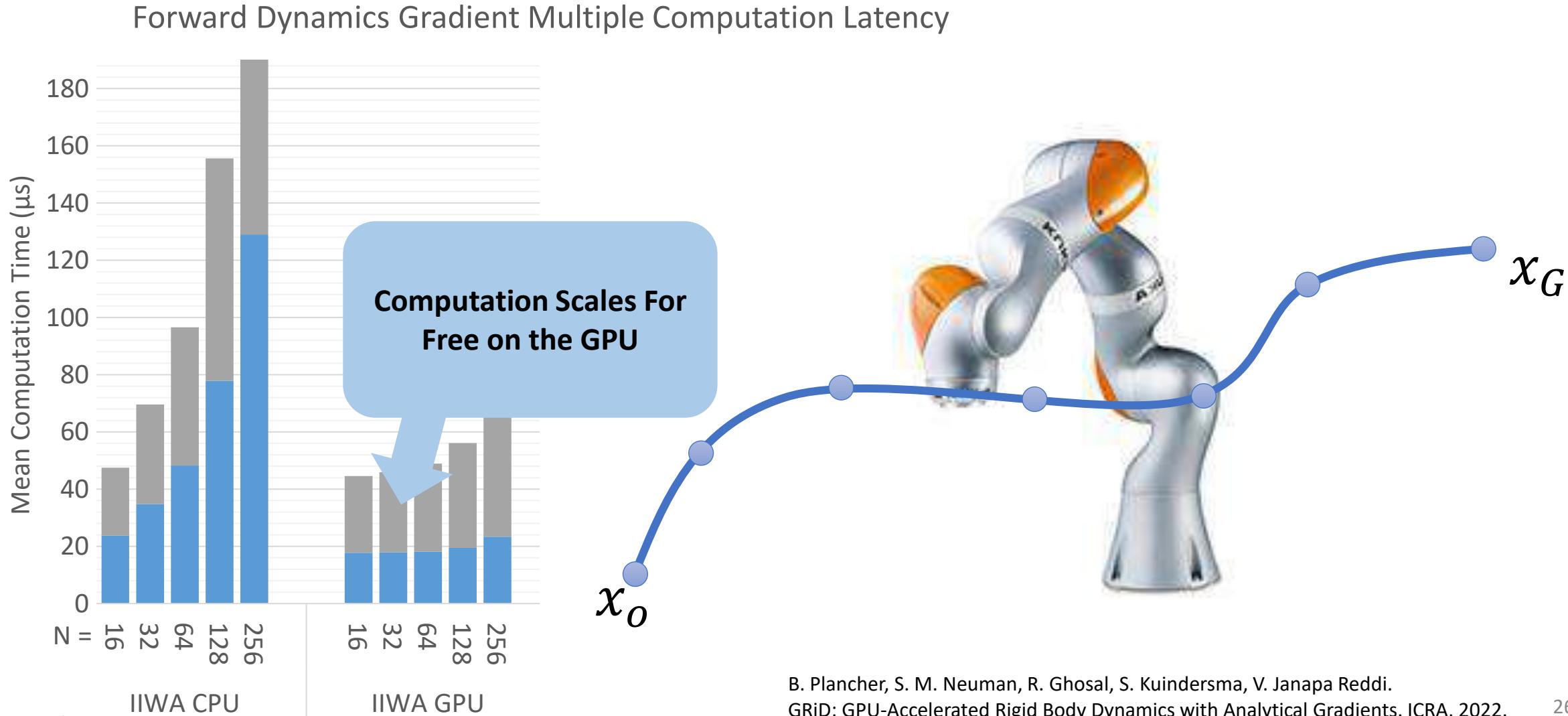
Chips and Salsa: Acceleration Beyond ASICs

Forward Dynamics Gradient Multiple Computation Latency



“Widgetism”: Avoid Over-Specialization

Chips and Salsa: Acceleration Beyond ASICs

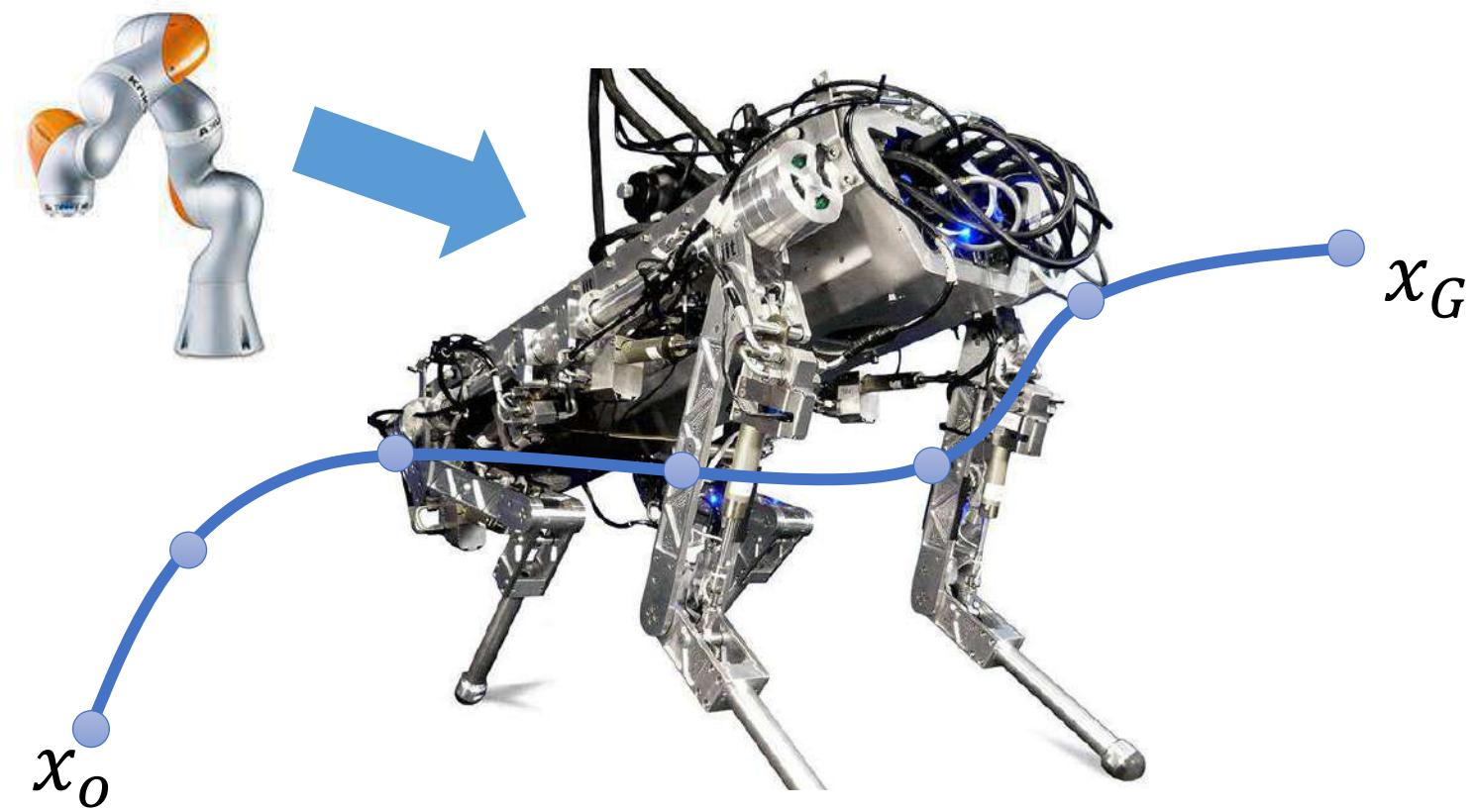
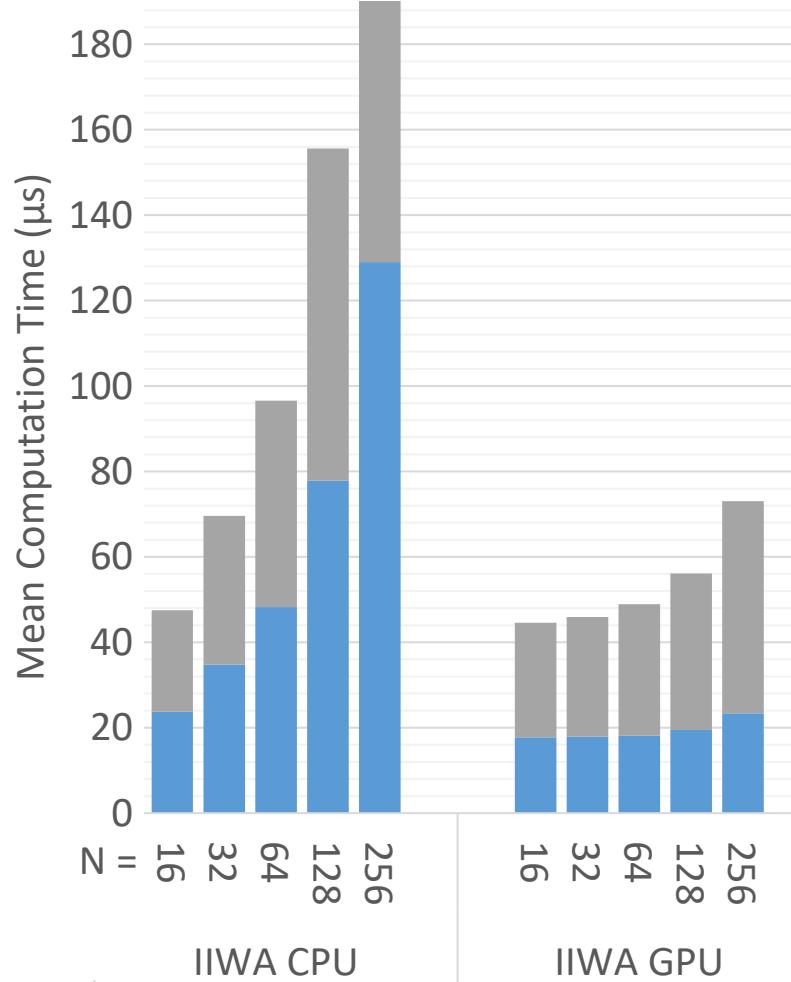


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Chips and Salsa: Acceleration Beyond ASICs

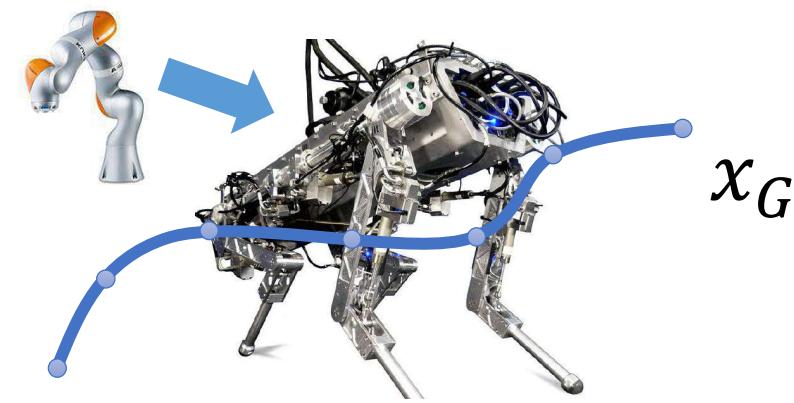
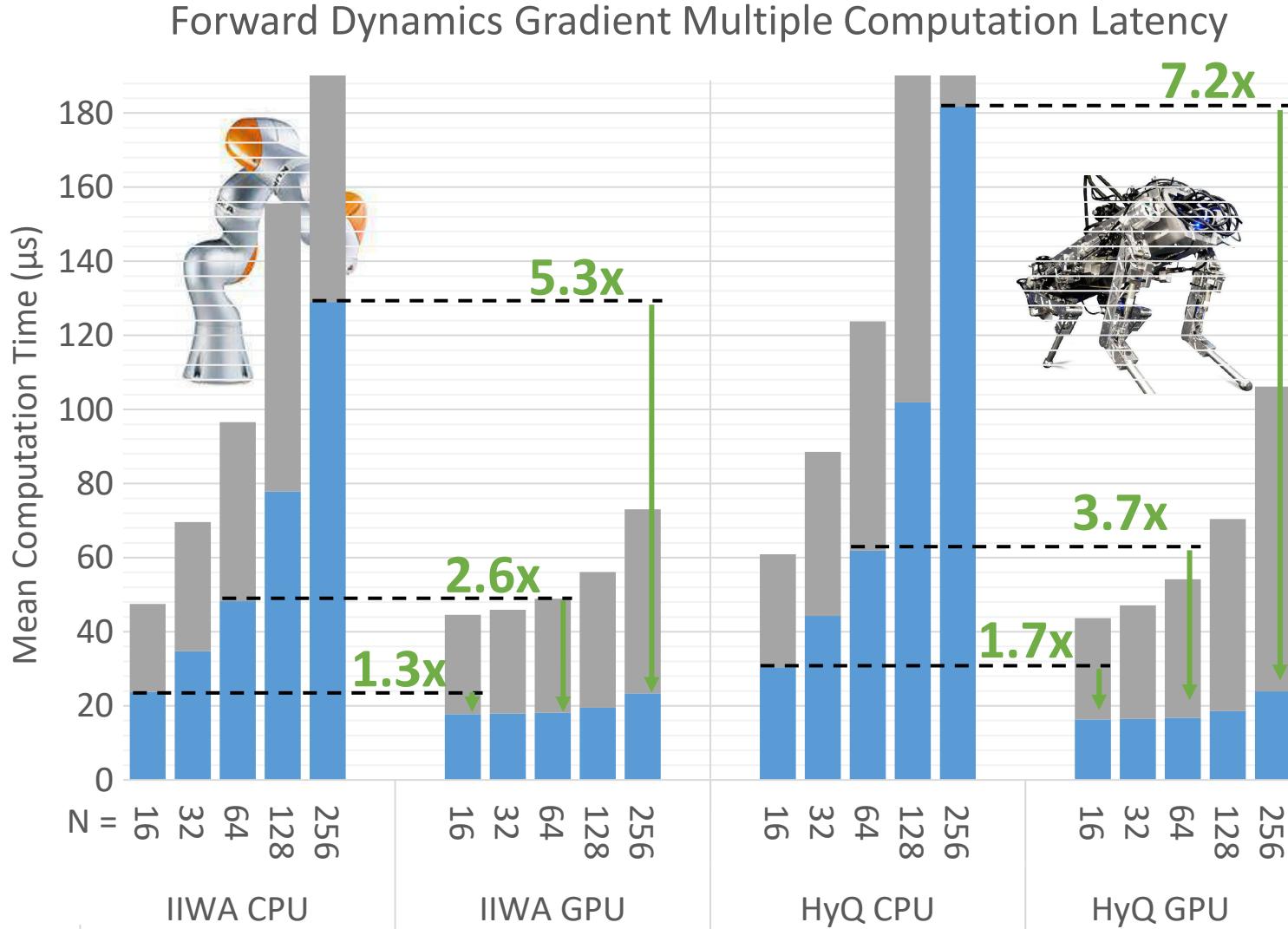
Forward Dynamics Gradient Multiple Computation Latency



3,5

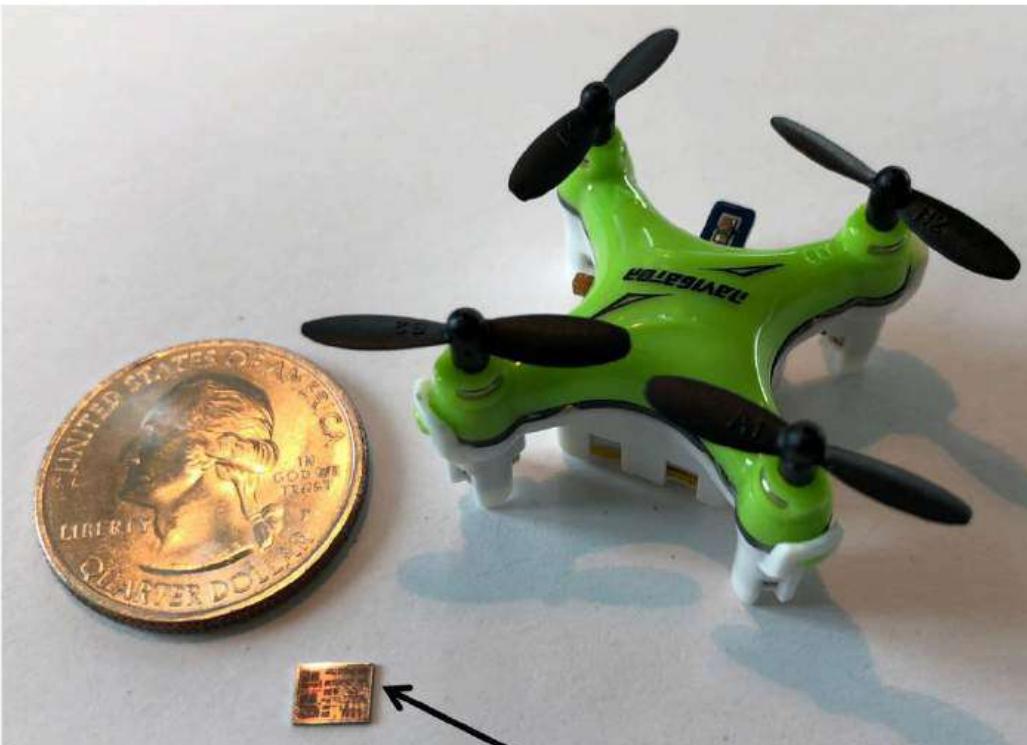
“Widgetism”: Avoid Over-Specialization

Chips and Salsa: Acceleration Beyond ASICs



“Widgetism”: Avoid Over-Specialization

Chips and Salsa: Acceleration Beyond ASICs



Visual-Inertial Odometry on Chip: An Algorithm-and-Hardware Co-design Approach

Zhengdong Zhang*, Amr Suleiman*, Luca Carlone, Vivienne Sze, Sertac Karaman
Massachusetts Institute of Technology, Cambridge, Massachusetts 02139
Emails: {zhangzd,suleiman,lcarlone,sze,sertac}@mit.edu, Website: <http://navion.mit.edu>

*These authors contributed equally to this work

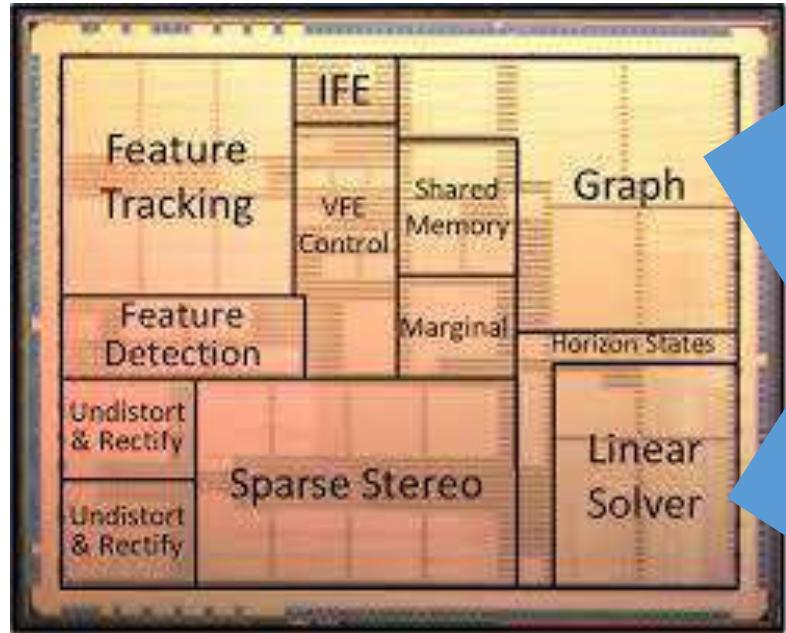
Navion: A 2mW Fully Integrated Real-Time Visual-Inertial Odometry Accelerator for Autonomous Navigation of Nano Drones

Amr Suleiman, Member, IEEE, Zhengdong Zhang, Student Member, IEEE, Luca Carlone, Member, IEEE
Sertac Karaman, Member, IEEE and Vivienne Sze, Senior Member, IEEE

3,5

“Widgetism”: Avoid Over-Specialization

Chips and Salsa: Acceleration Beyond ASICs



Fundamental Graph
Operations and
Linear Algebra **WILL**
be highly portable!

3,5

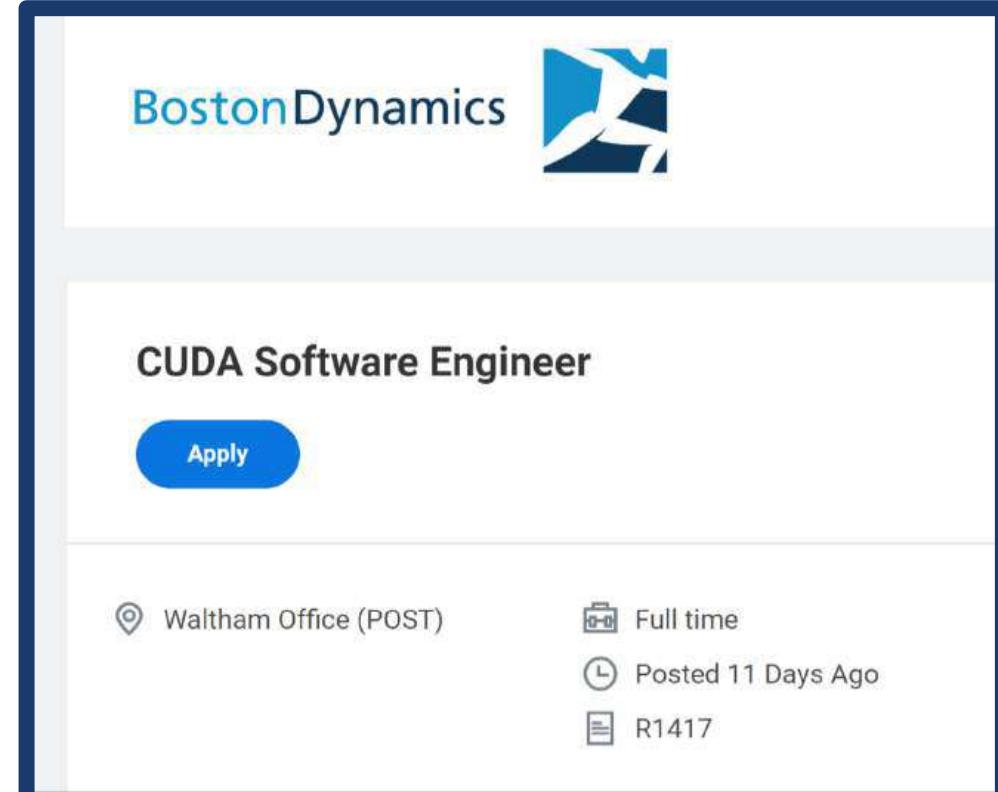
“Widgetism”: Avoid Over-Specialization

Chips and Salsa: Acceleration Beyond ASICs



Transition to industry and real impact is a challenge!

GPUs in robots is a
VERY NEW thing!



BostonDynamics

CUDA Software Engineer

Apply

Waltham Office (POST)

Full time

Posted 11 Days Ago

R1417

A screenshot of a job listing for a CUDA Software Engineer position at Boston Dynamics. The listing includes the company logo, the job title, an 'Apply' button, location information, and other details like full-time status and posting date.

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4 Pump the Brakes: Do Not Always Accelerate

Pitfall: Assume accelerators always improve total system performance.



6 Forest vs. Trees: Take an End-to-End View

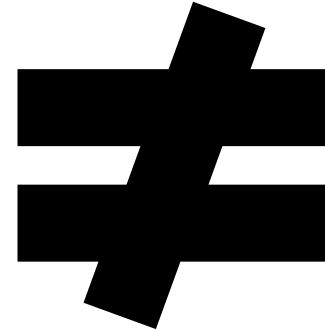
Pitfall: A narrow scope: acceleration begins and ends with compute.

4,6

Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



ML



ML
Code

4,6

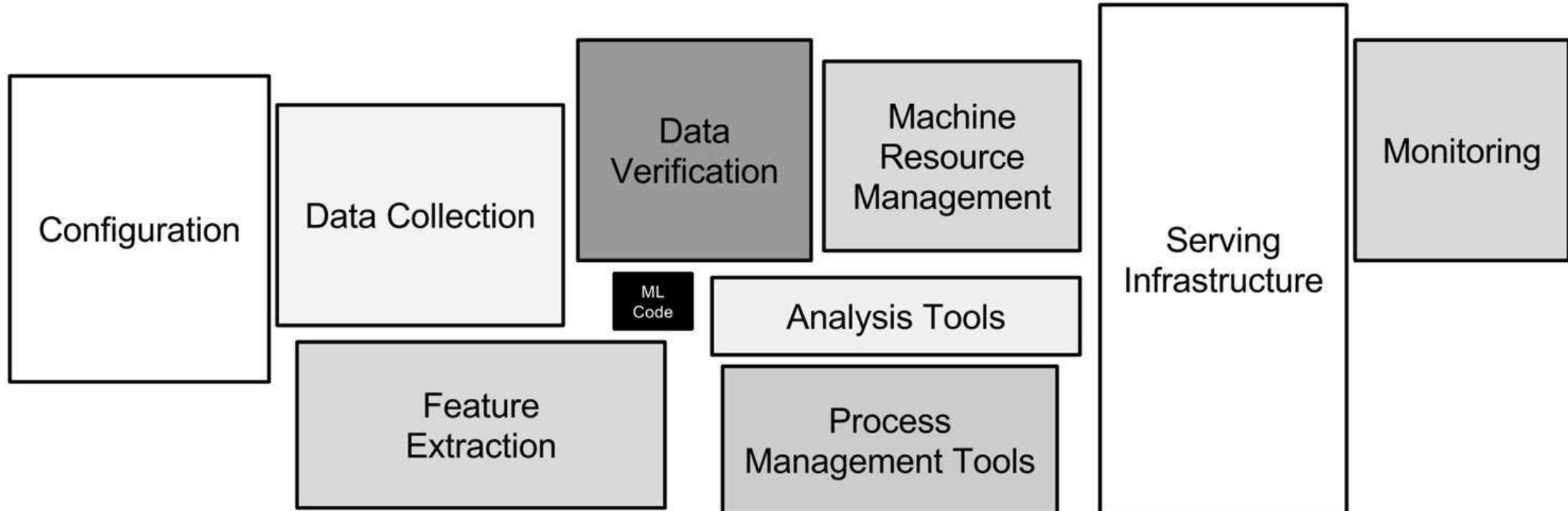
Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



ML
Code

Pump the Brakes: Do Not Always Accelerate

Forest vs. Trees: Take an End-to-End View



Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View

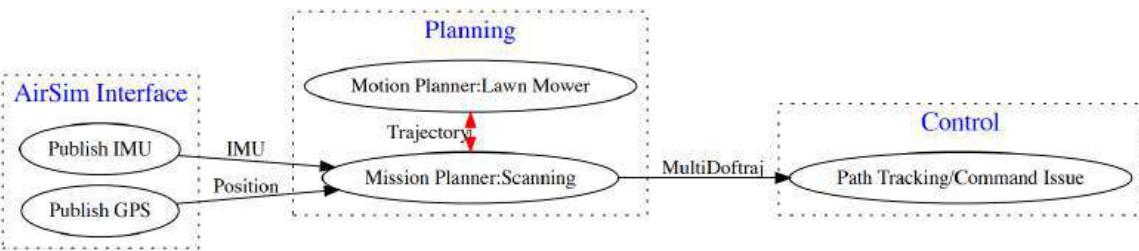


Amdahl's Law

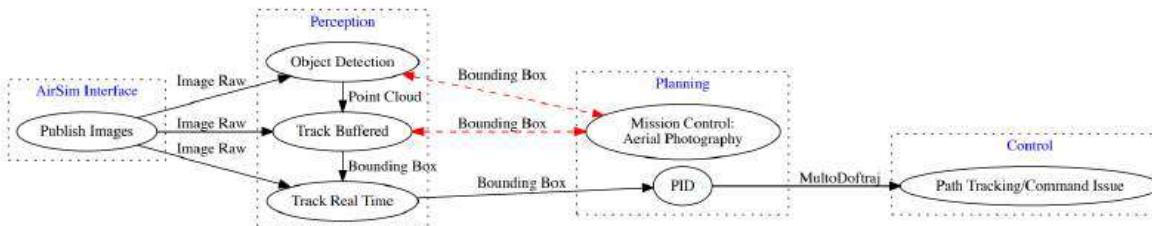
$$\text{speedup}(f,n) = \frac{1}{(1-f)+\frac{f}{n}}$$

f = fraction of the program that is parallelizable
 n = parallel processors

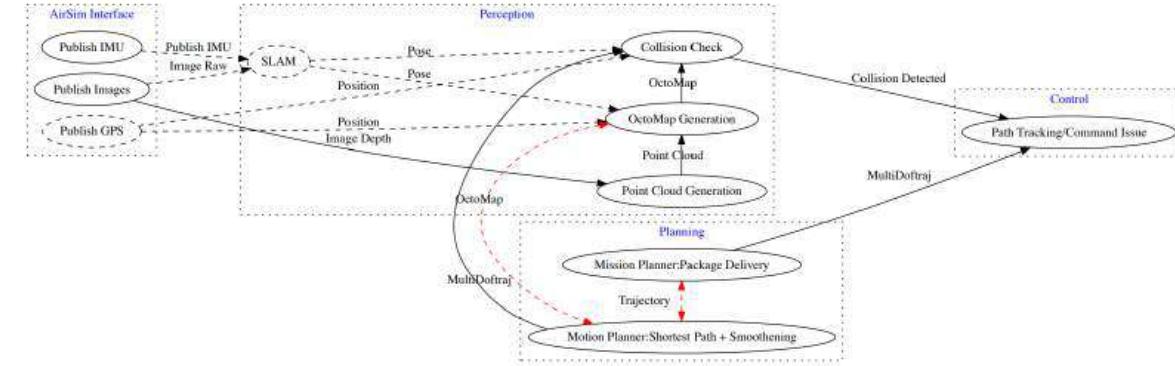
Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



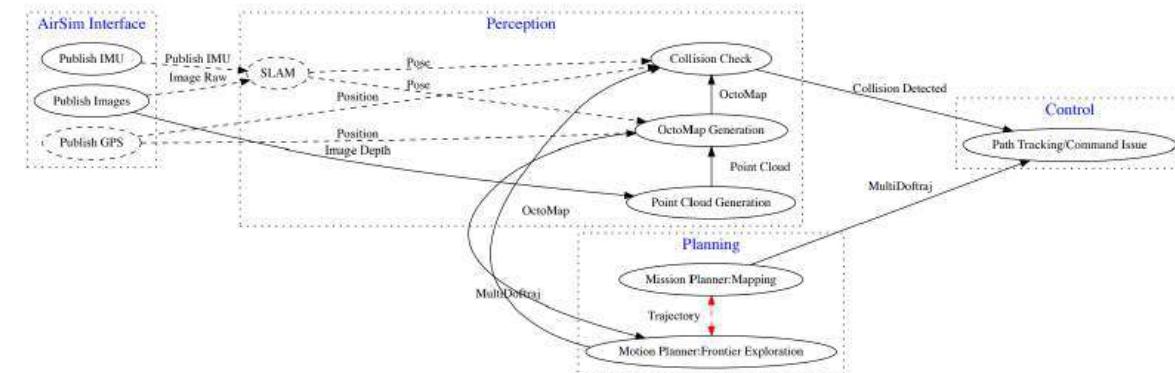
(a) Scanning.



(b) Aerial Photography.



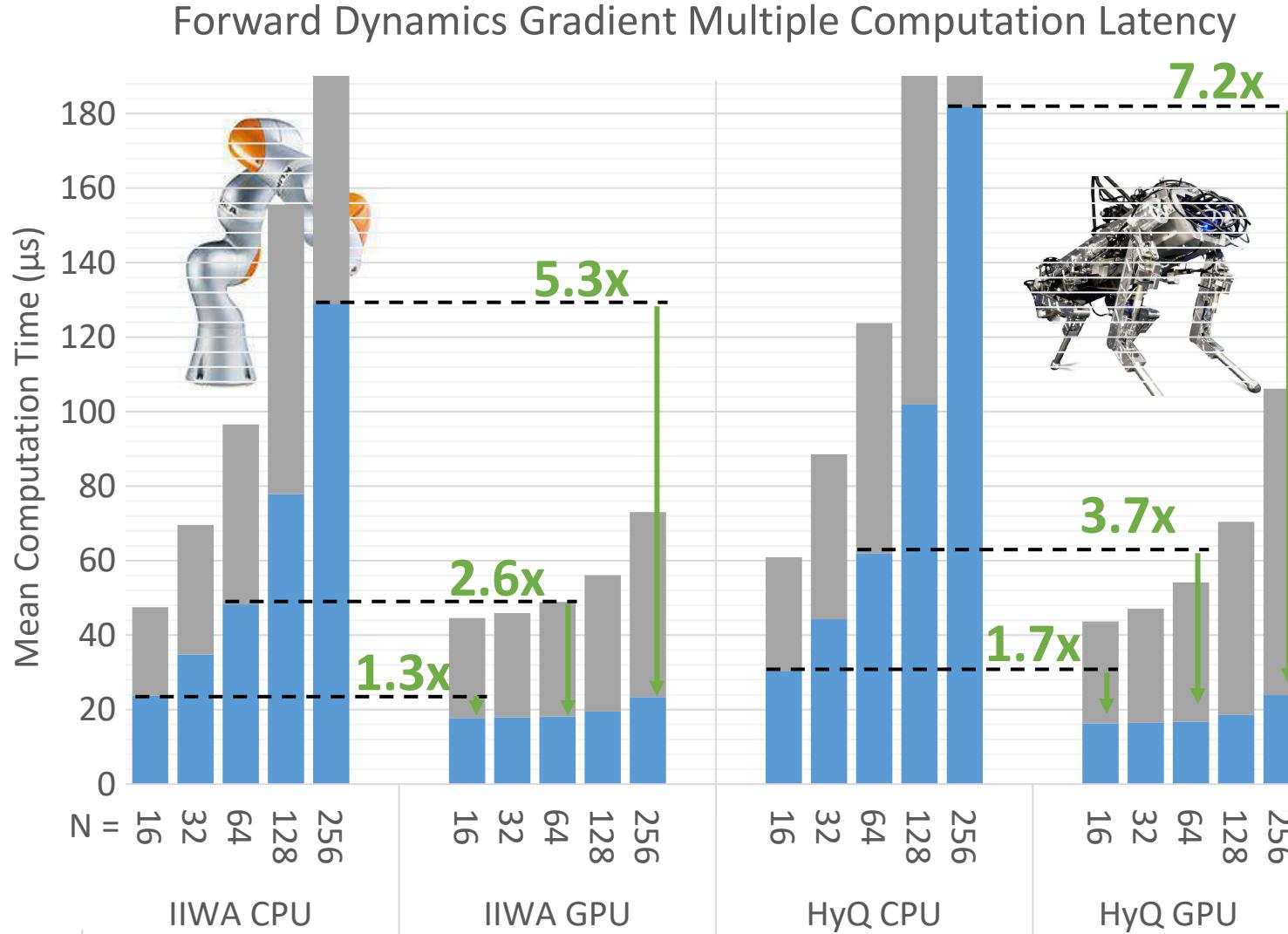
(c) Package Delivery.



(d) 3D Mapping.

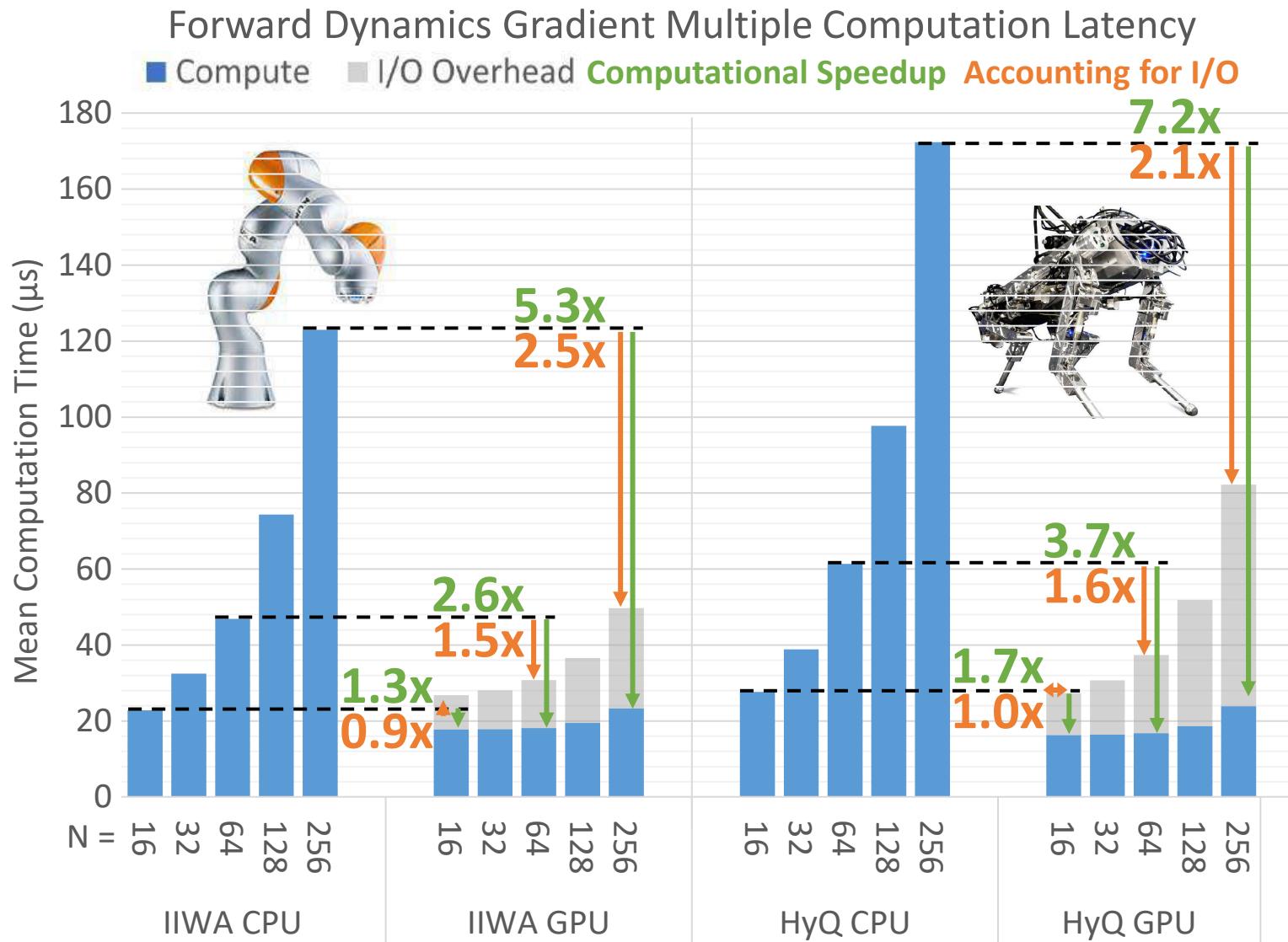
6

Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



What about I/O?

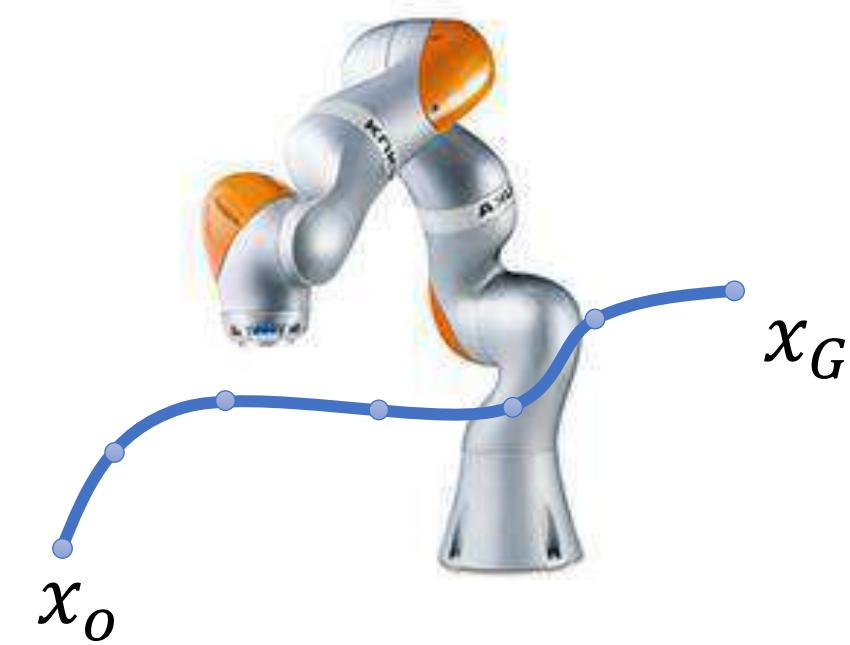
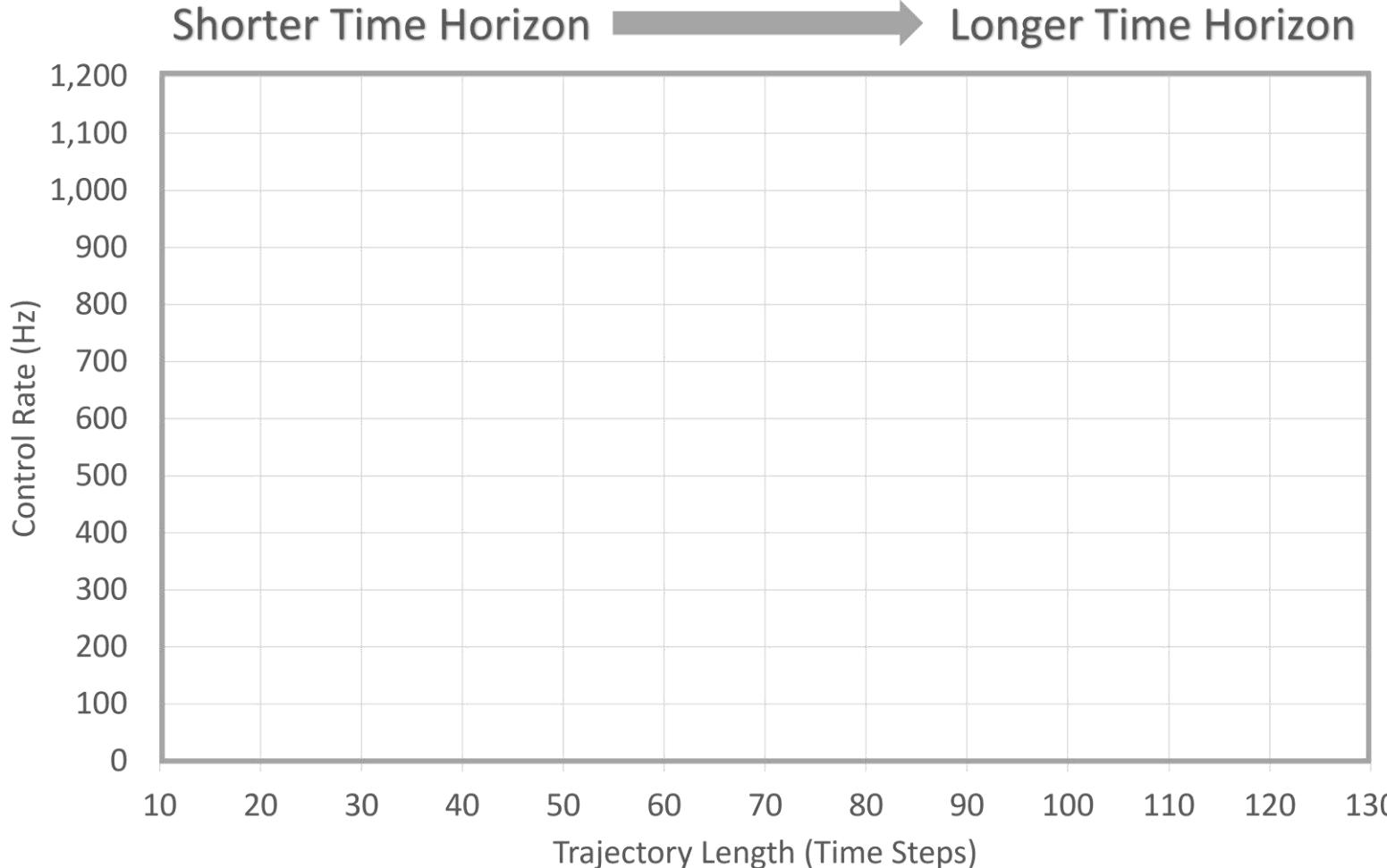
Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



I/O Matters
A LOT!

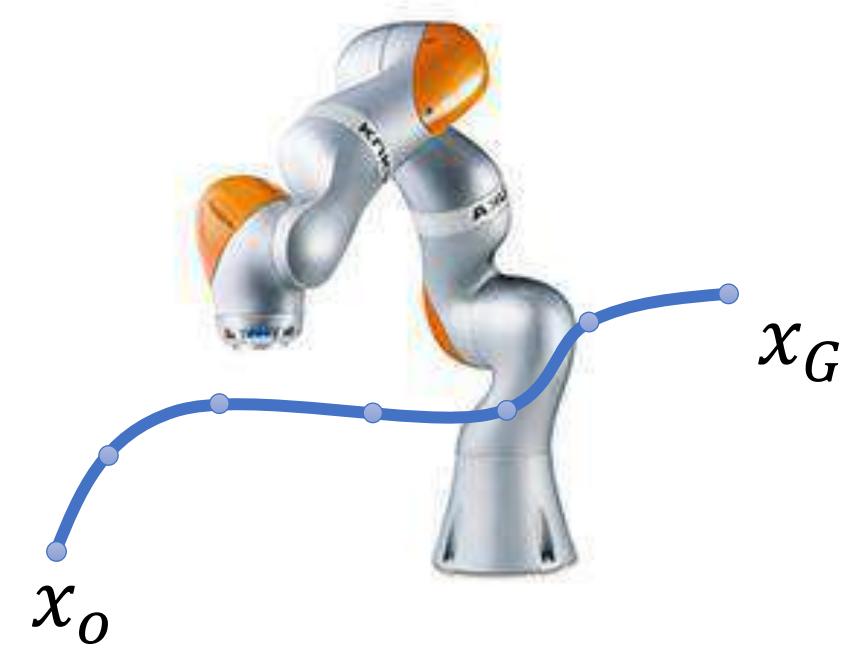
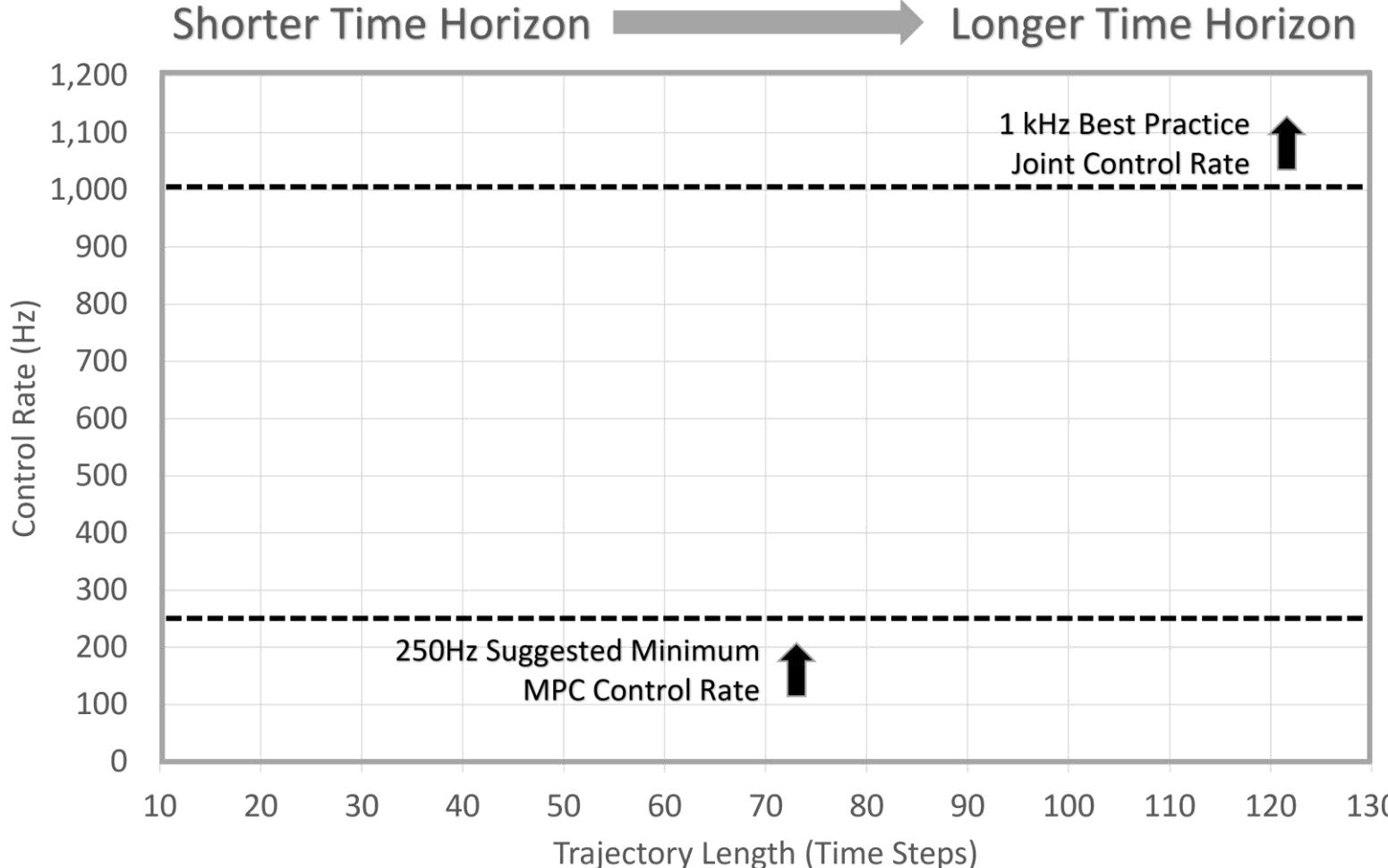
4

Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



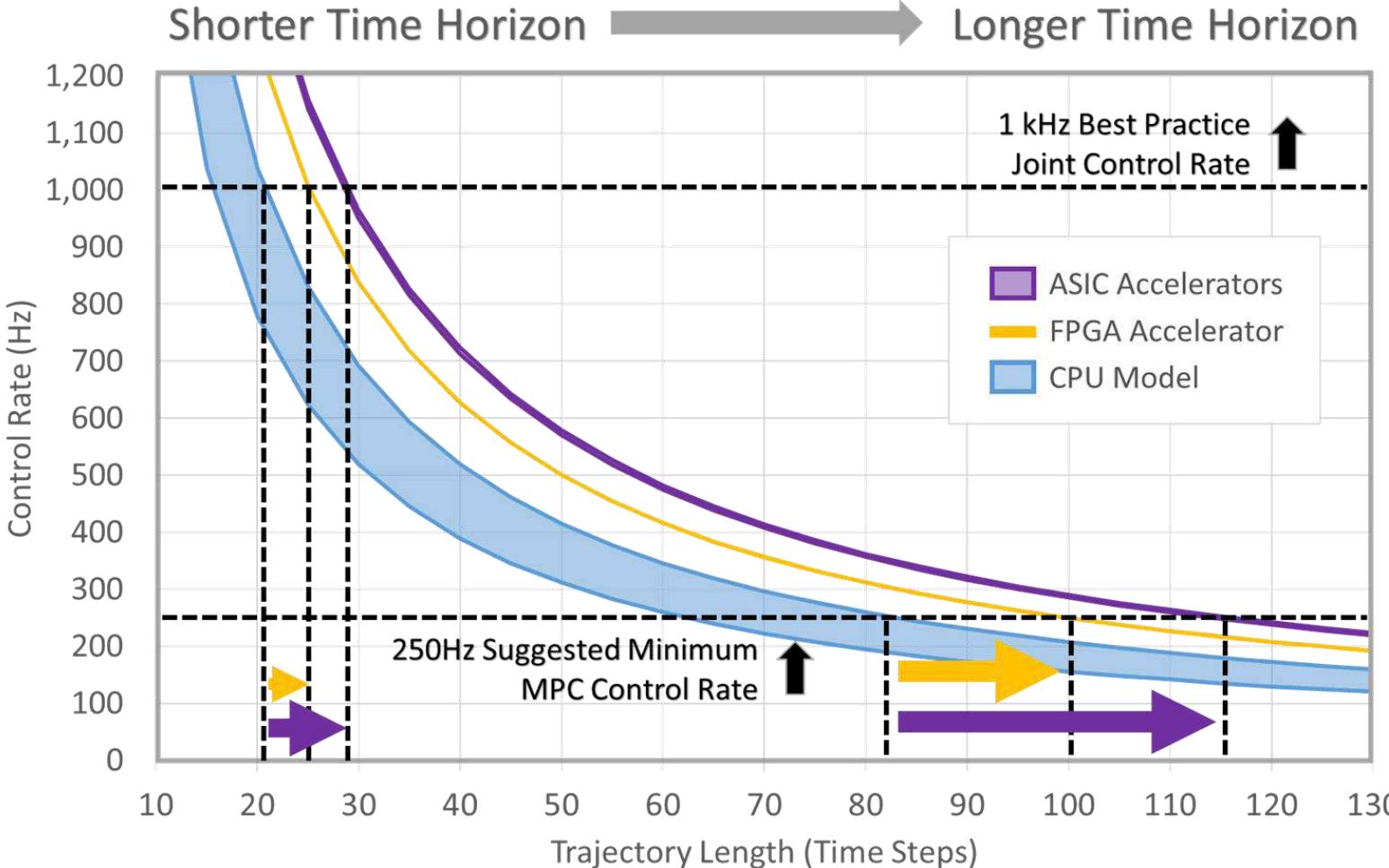
4

Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



4

Pump the Brakes: Do Not Always Accelerate Forest vs. Trees: Take an End-to-End View



Does this matter?
How can we evaluate
this impact?





② Measure Twice, Cut Once: Metrics Matter

Pitfall: Only focus on improving throughput or energy-delay product.

2 Measure Twice, Cut Once: Metrics Matter



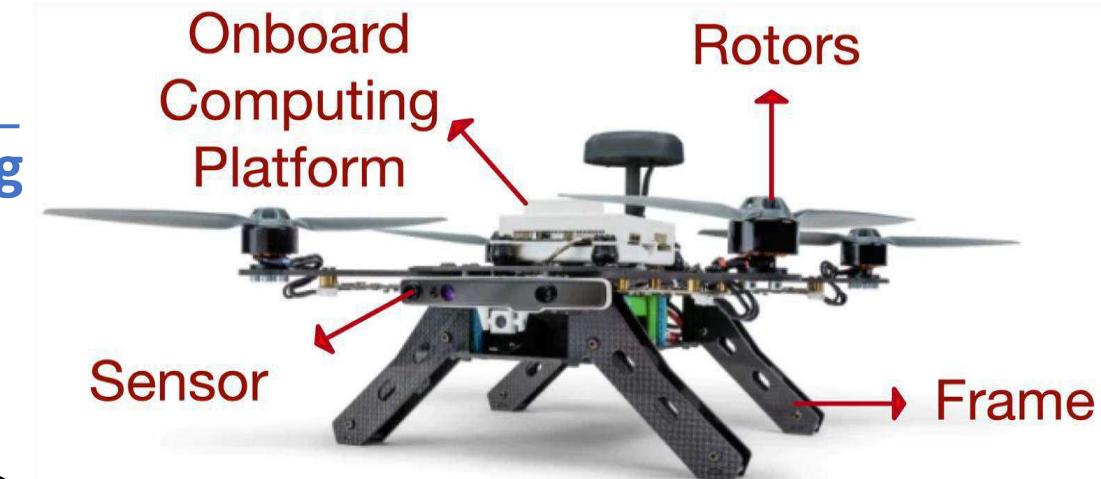
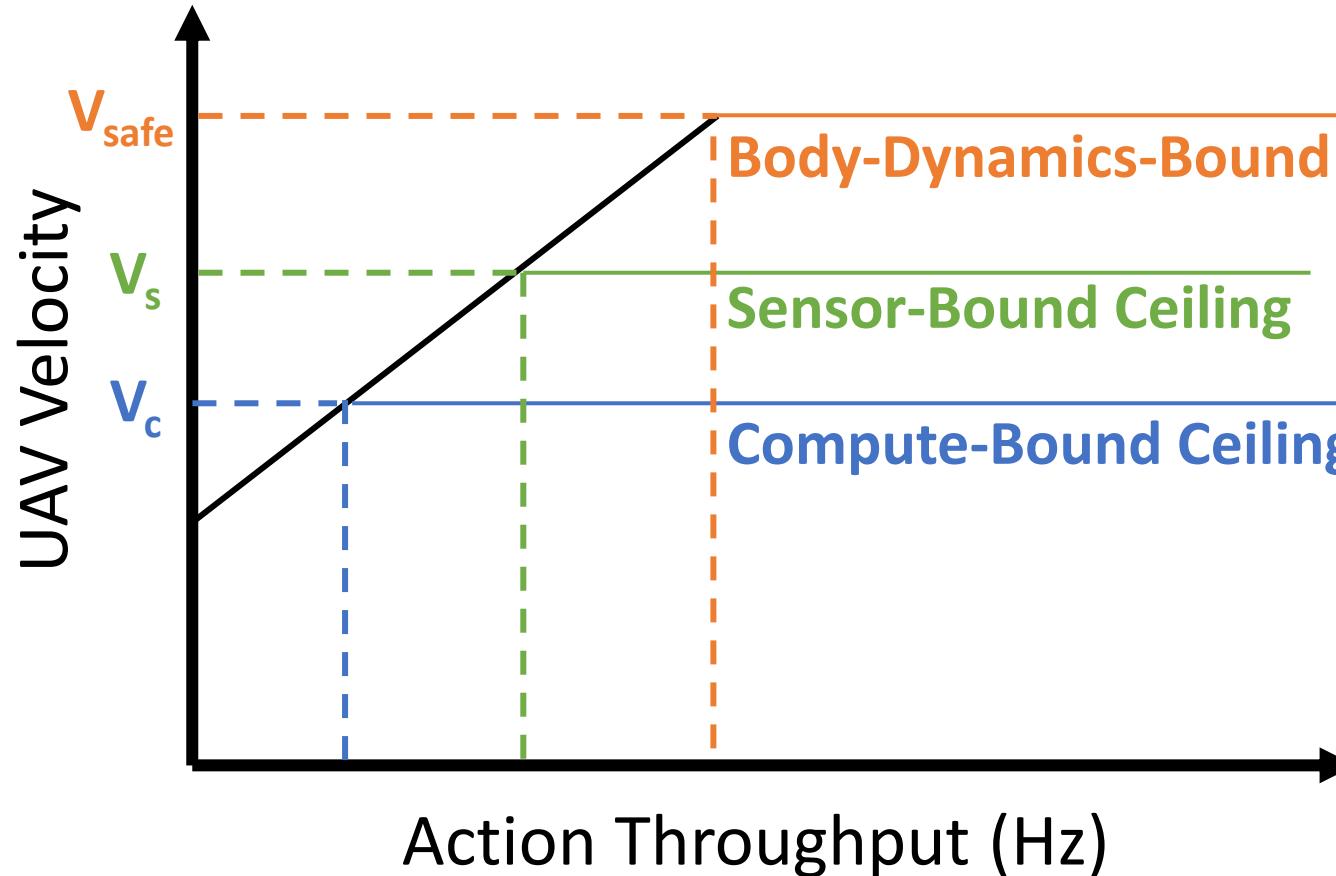
Roofline Model for UAVs: A Bottleneck Analysis Tool for Onboard Compute Characterization of Autonomous Unmanned Aerial Vehicles

"All models are wrong, but some are useful." – George Box

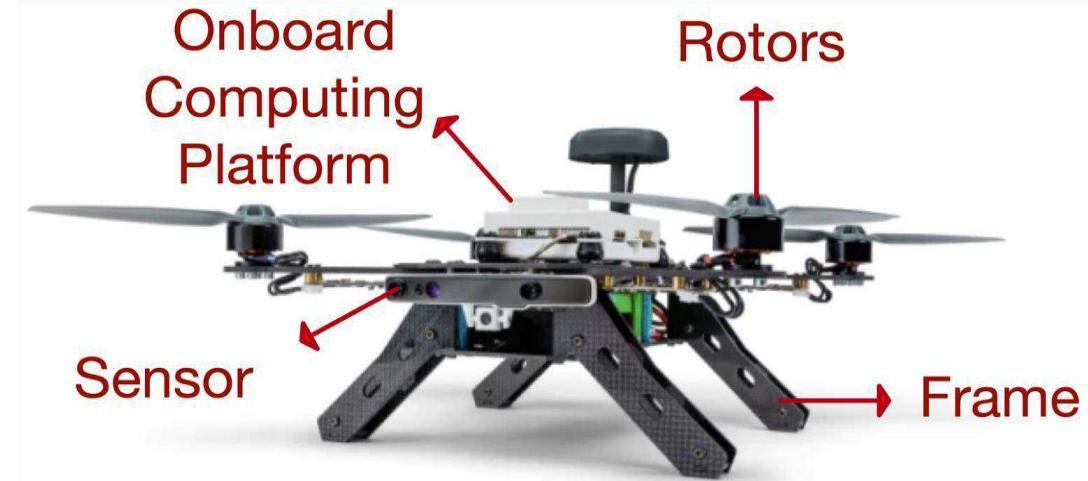
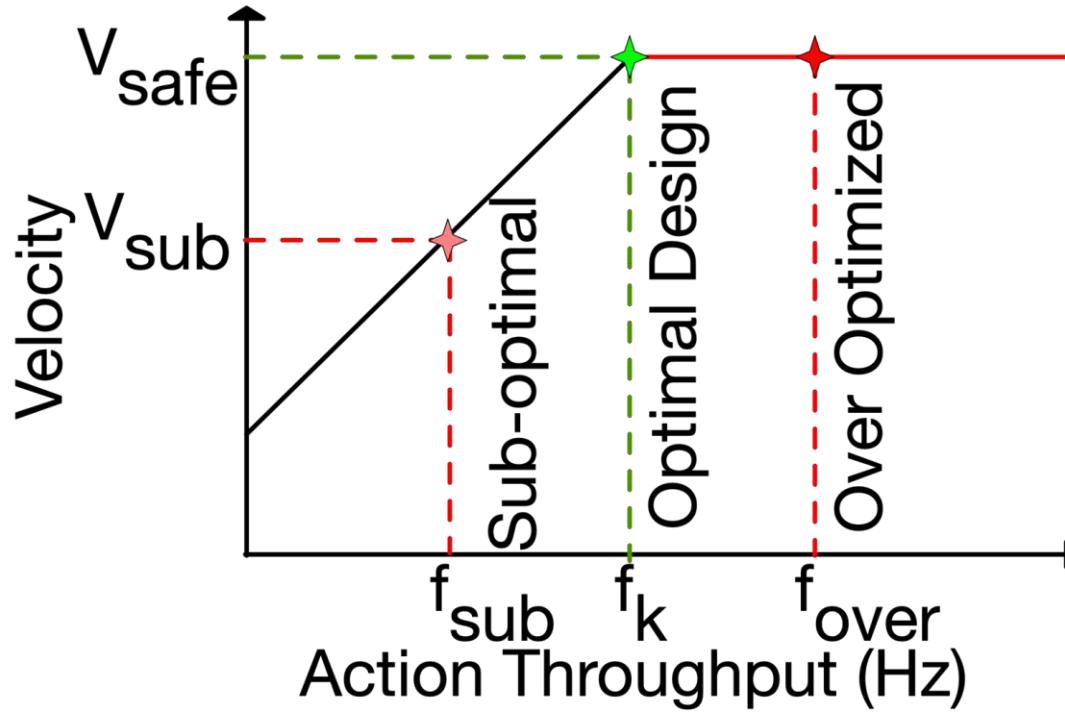
Srivatsan Krishnan[†] Zishen Wan^{*†} Kshitij Bhardwaj[‡] Ninad Jadhav[†] Aleksandra Faust[§] Vijay Janapa Reddi[†]

[†]Harvard University [‡]Lawrence Livermore National Lab [§]Google Brain Research

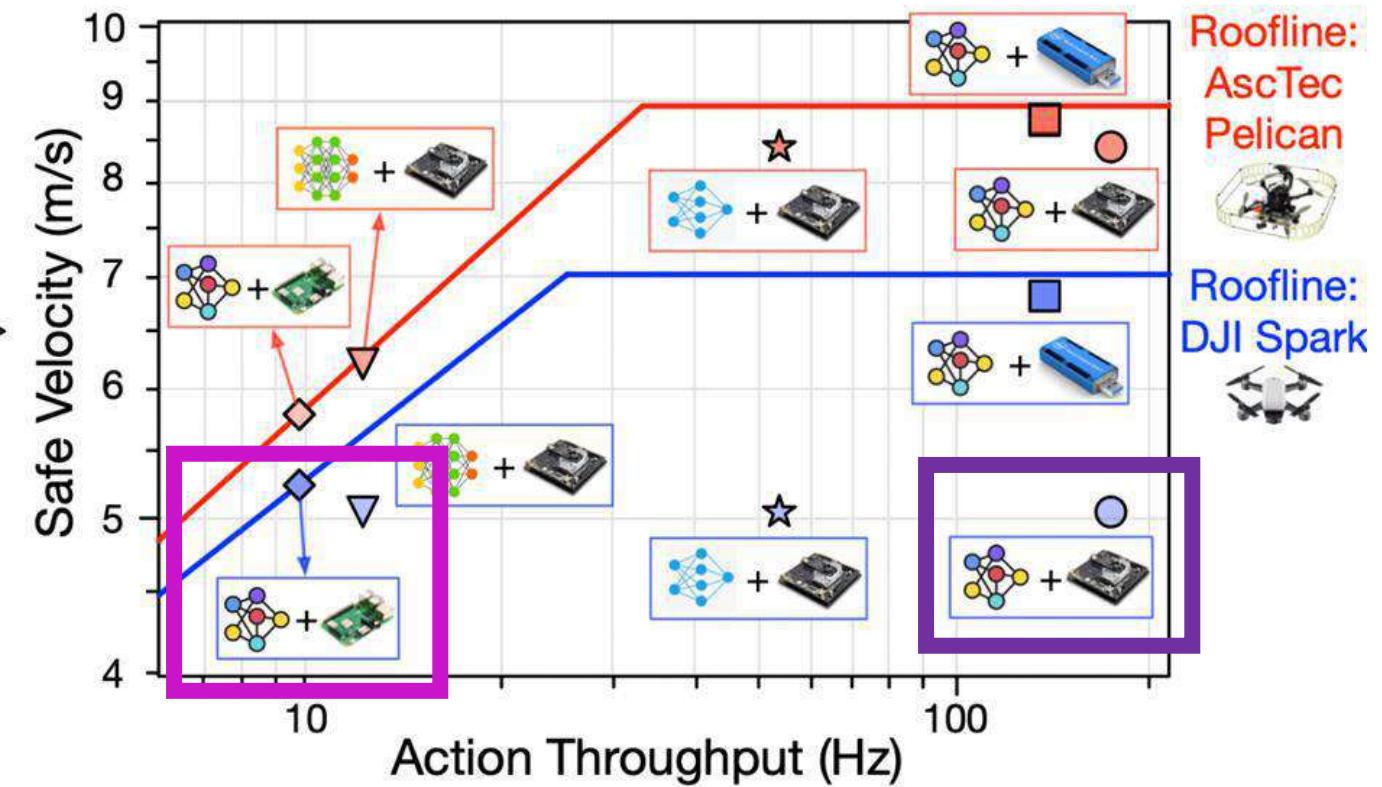
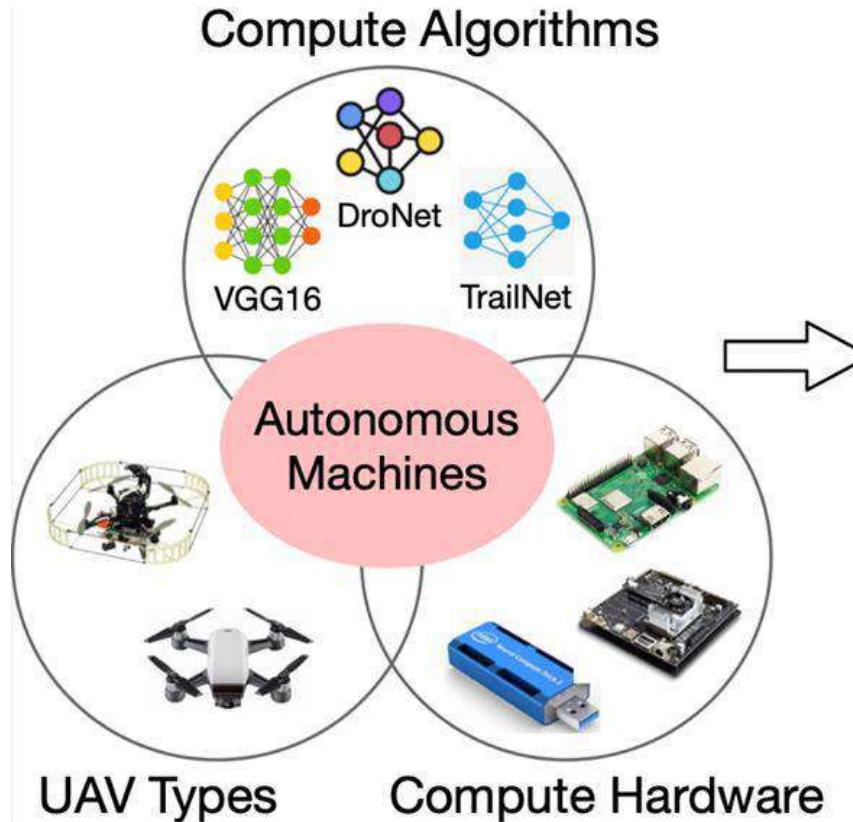
② Measure Twice, Cut Once: Metrics Matter



② Measure Twice, Cut Once: Metrics Matter



2 Measure Twice, Cut Once: Metrics Matter

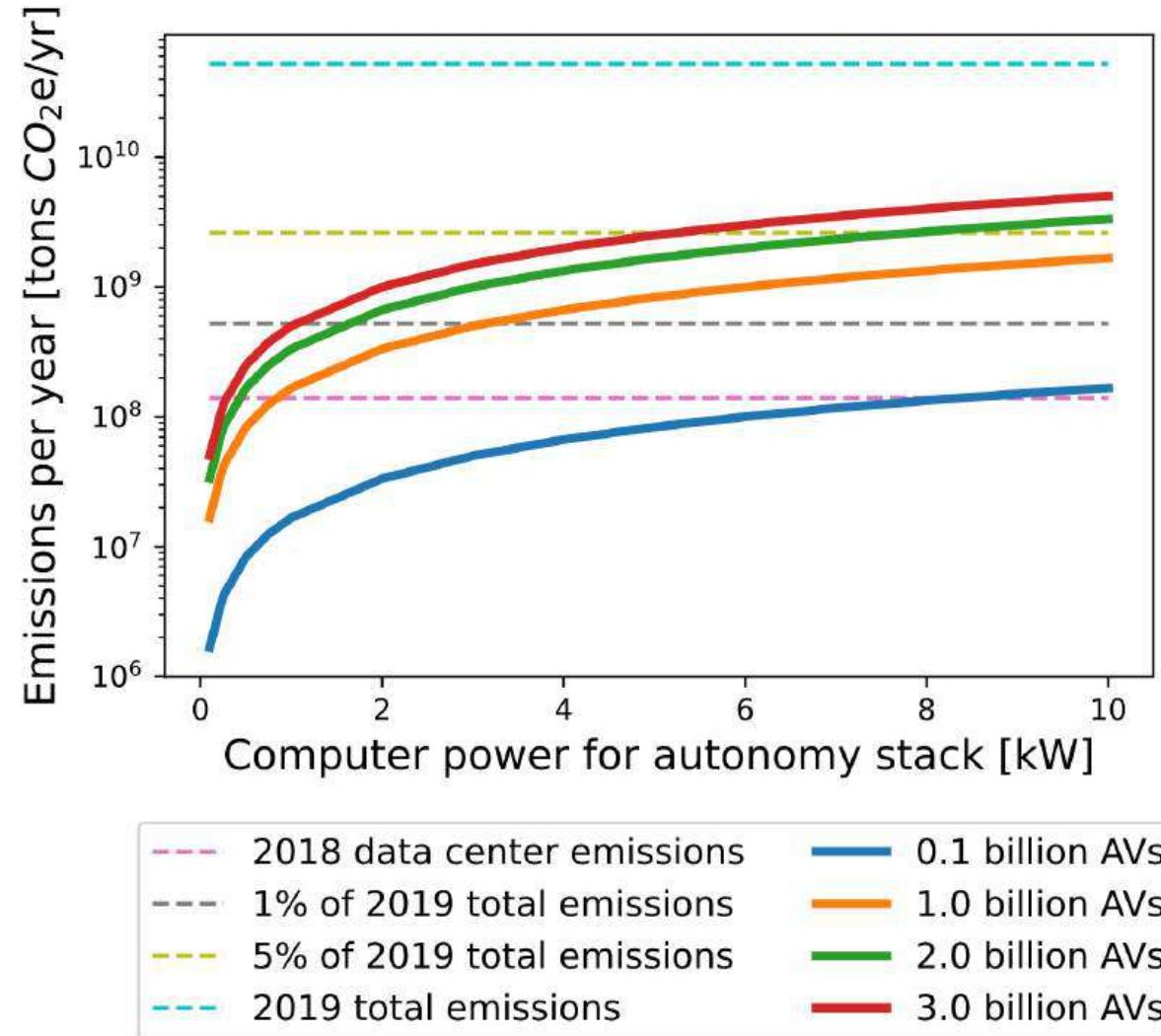


- ① Build Bridges: Engage with Domain Experts
- ② **Measure Twice, Cut Once: Metrics Matter**
- ③ “Widgetism”: Avoid Over-Specialization
- ④ **Pump the Brakes: Do Not Always Accelerate**
- ⑤ Chips and Salsa: Acceleration Beyond ASICs
- ⑥ **Forest vs. Trees: Take an End-to-End View**
- ⑦ Design Global: Sustainability and Impact



Pitfall: Design compute in isolation from its global and societal impact.

7 Design Global: Sustainability and Impact





7 Design Global: Sustainability and Impact

TABLE 1. FAR for CS and engineering subfields based on prior work and including our result for robotics [1], [3], [4] (data from 2017 to 2023).

FIELD	FAR (%)
CS education	42
Human–computer interaction	26
CS overall average	16–26
Knowledge systems	19
Software engineering and languages	14
Artificial intelligence	12
Robotics	11–12 (our analysis)
Computer systems	10
Theory and algorithms	8

As has been noted in related works, this kind of methodology has many flaws and does not take into account much of the nuance in gender, including issues of bias, misperception, and nonbinary identities [7], [8]. However, we hope that this initial study will help add to the robotics community's understanding of the current state of gender diversity and, at a minimum, provide directionally correct data to help with future diversity, equity, and inclusion efforts.

The Magnificent Seven

Challenges & Opportunities in Domain-Specific Accelerator Design for Autonomous Systems



- ① Build Bridges: Engage with Domain Experts
 - ② Measure Twice, Cut Once: Metrics Matter
 - ③ “Widgetize” the Application
 - ④ Pump the Performance
 - ⑤ Chips and Tools for FPGAs and ASICs
 - ⑥ Forest vs. Trees. Take an End-to-End View
 - ⑦ Design Global: Sustainability and Impact
- So What Can We Do?**

The Magnificent Seven

Challenges & Opportunities in Domain-Specific Accelerator Design for Autonomous Systems



Future Directions and Opportunities

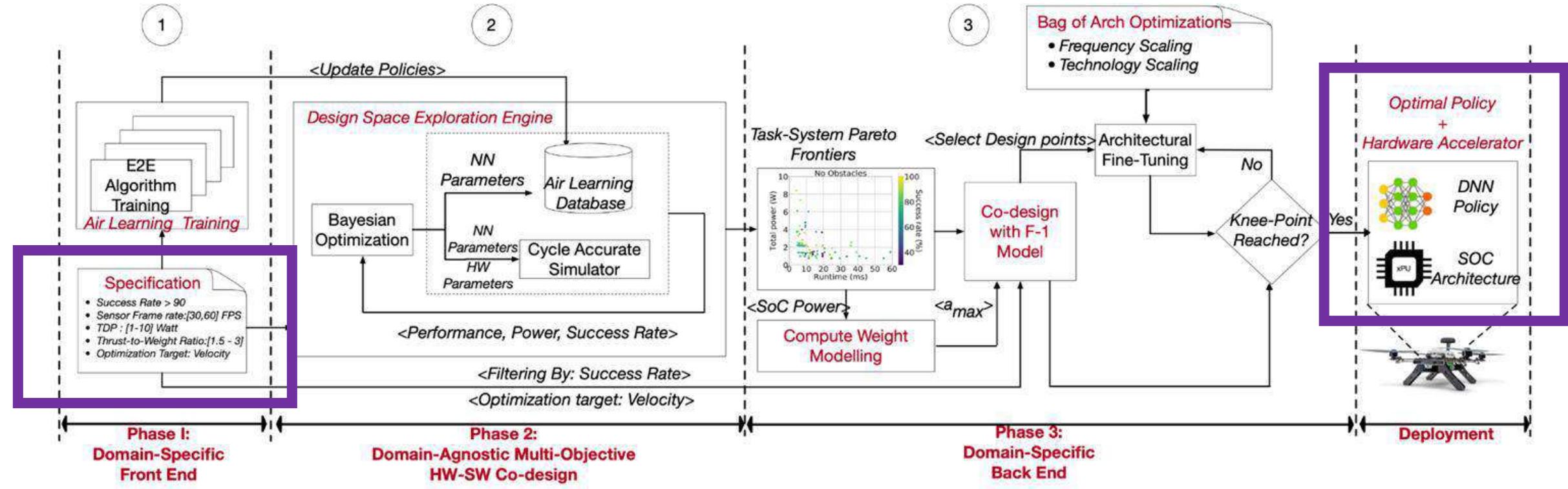
- Ⓐ Enabling Technologies and Methodologies
- Ⓑ Fostering a Robust Research Ecosystem
- Ⓒ Sustainable & Responsible Hardware Design



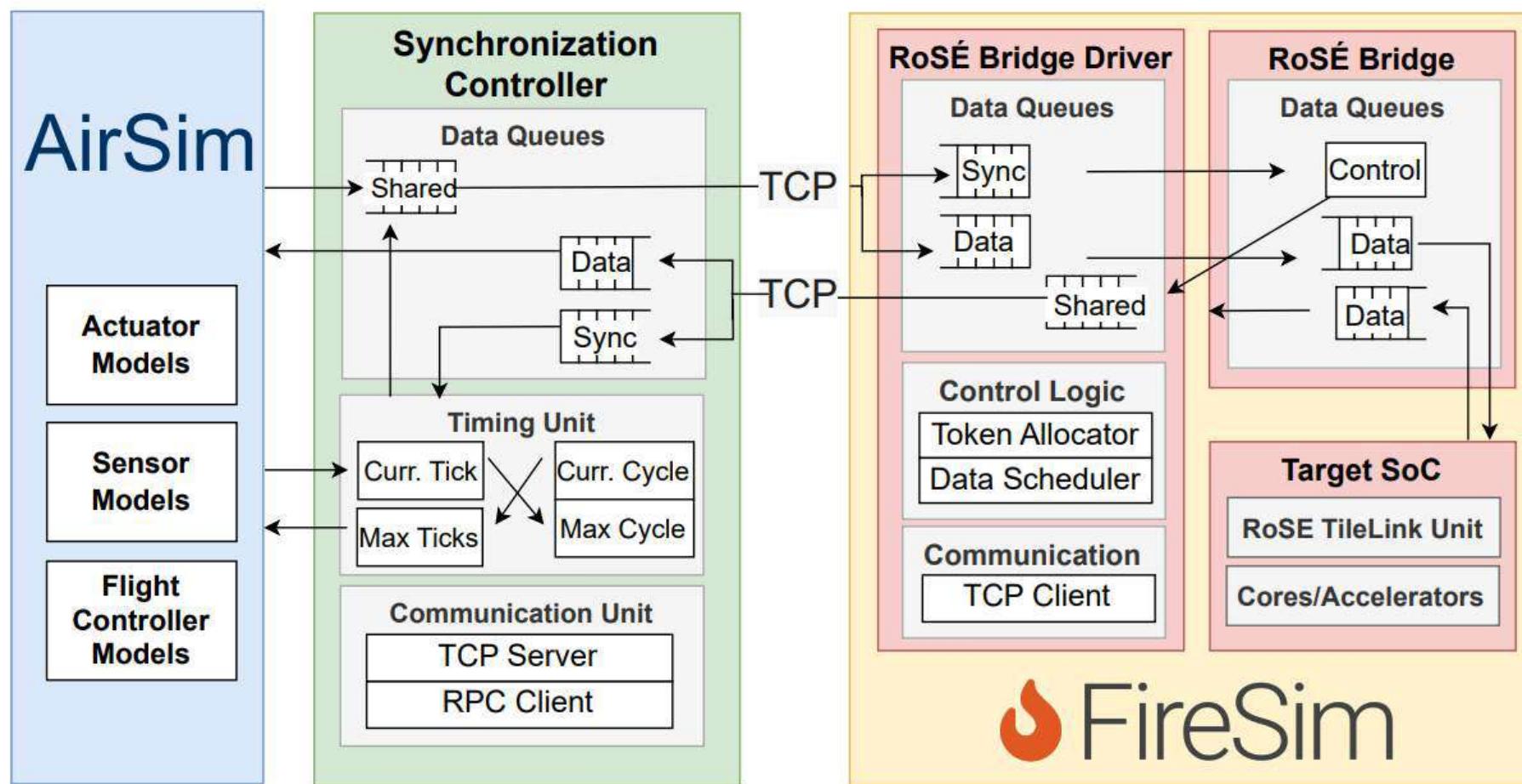
A Enabling Technologies and Methodologies

***Opportunity:** Reduce Complexity and Time-to-Market with Agile Design Tools and End-to-End Simulation Environments*

A Enabling Technologies and Methodologies



A Enabling Technologies and Methodologies





*Opportunity: Increase Cross-Domain
Collaborations and Develop Open
Source Resources and Benchmarks*



B Fostering a Robust Research Ecosystem

ACM SIGARCH

BENEFIT ▾ CONTRIB

CALL FOR PAPERS:

RoboARCH (co-located with MICRO 2022)

October 2, 2022 in MICRO 2022 in Chicago, IL

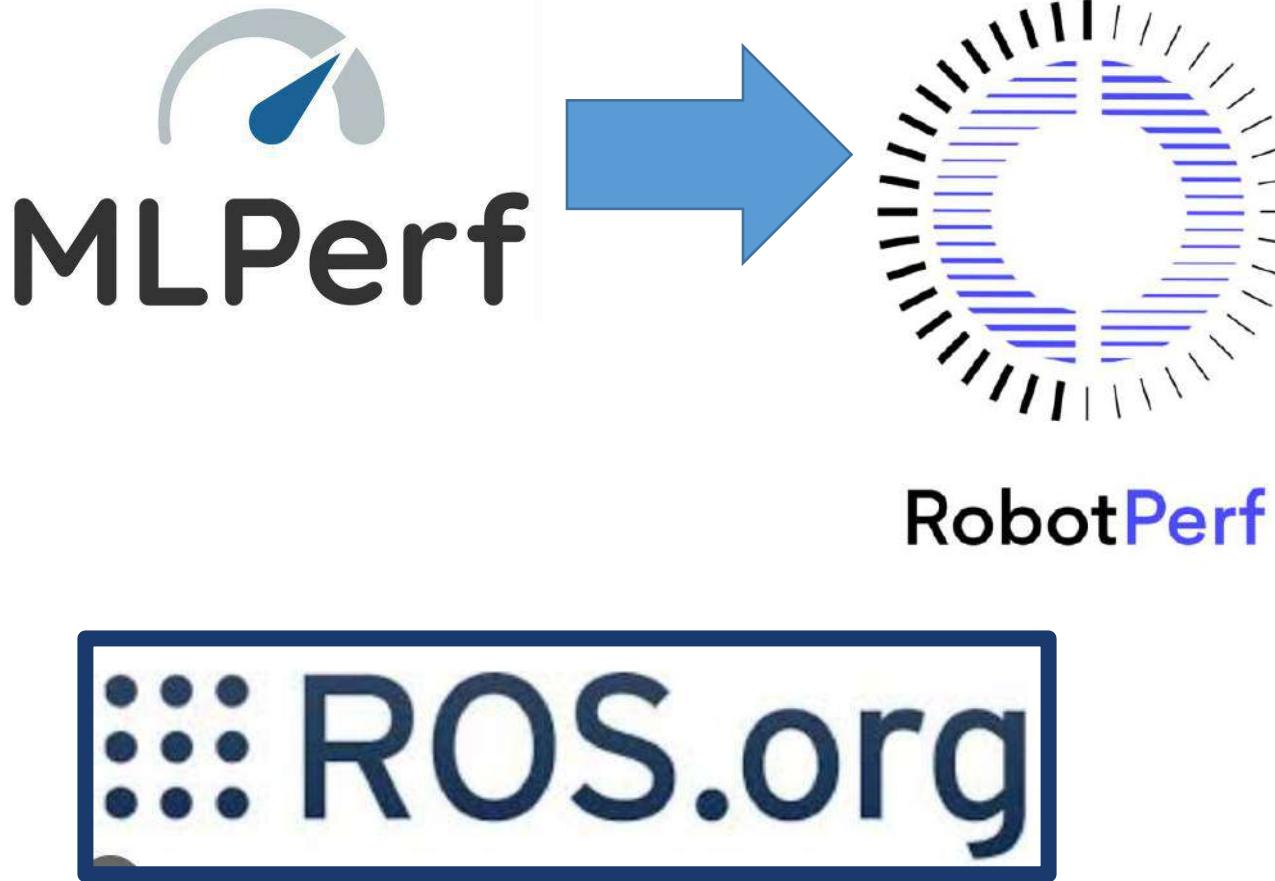
URL: <https://sites.google.com/g.harvard.edu/roboarch>

We'll be at
MICRO 2024!





B Fostering a Robust Research Ecosystem



RobotPerf: An Open-Source, Vendor-Agnostic, Benchmarking Suite
for Evaluating Robotics Computing System Performance

Víctor Mayoral-Vilches^{1,2}, Jason Jabbour³, Yu-Shun Hsiao³, Zishen Wan⁴, Martín Crespo-Álvarez¹,
Matthew Stewart³, Juan Manuel Reina-Muñoz¹, Prateek Nagras¹, Gaurav Vikhe¹,
Mohammad Bakhshaliipour⁵, Martin Pinzger², Stefan Rass^{6,2}, Smruti Panigrahi⁷, Giulio Corradi⁸,
Niladri Roy⁹, Phillip B. Gibbons⁵, Sabrina M. Neuman¹⁰, Brian Plancher¹¹, Vijay Janapa Reddi³

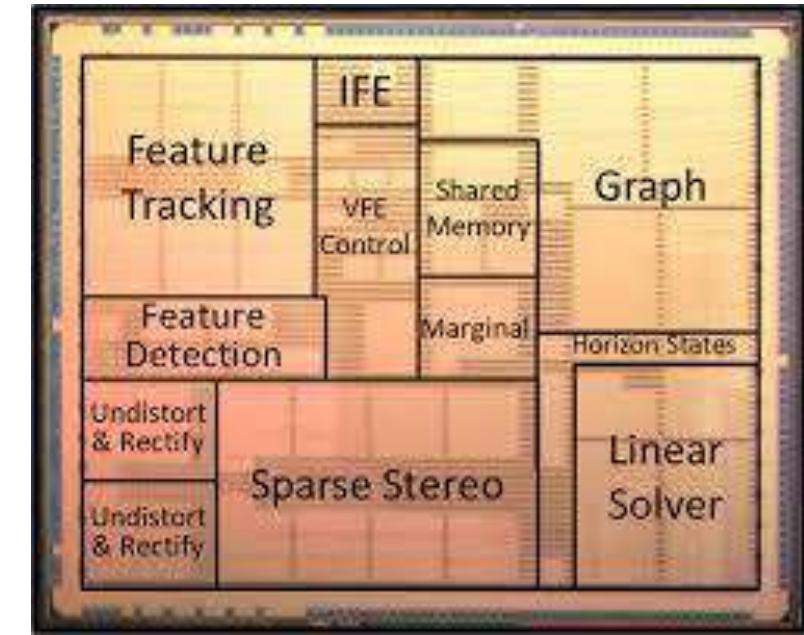
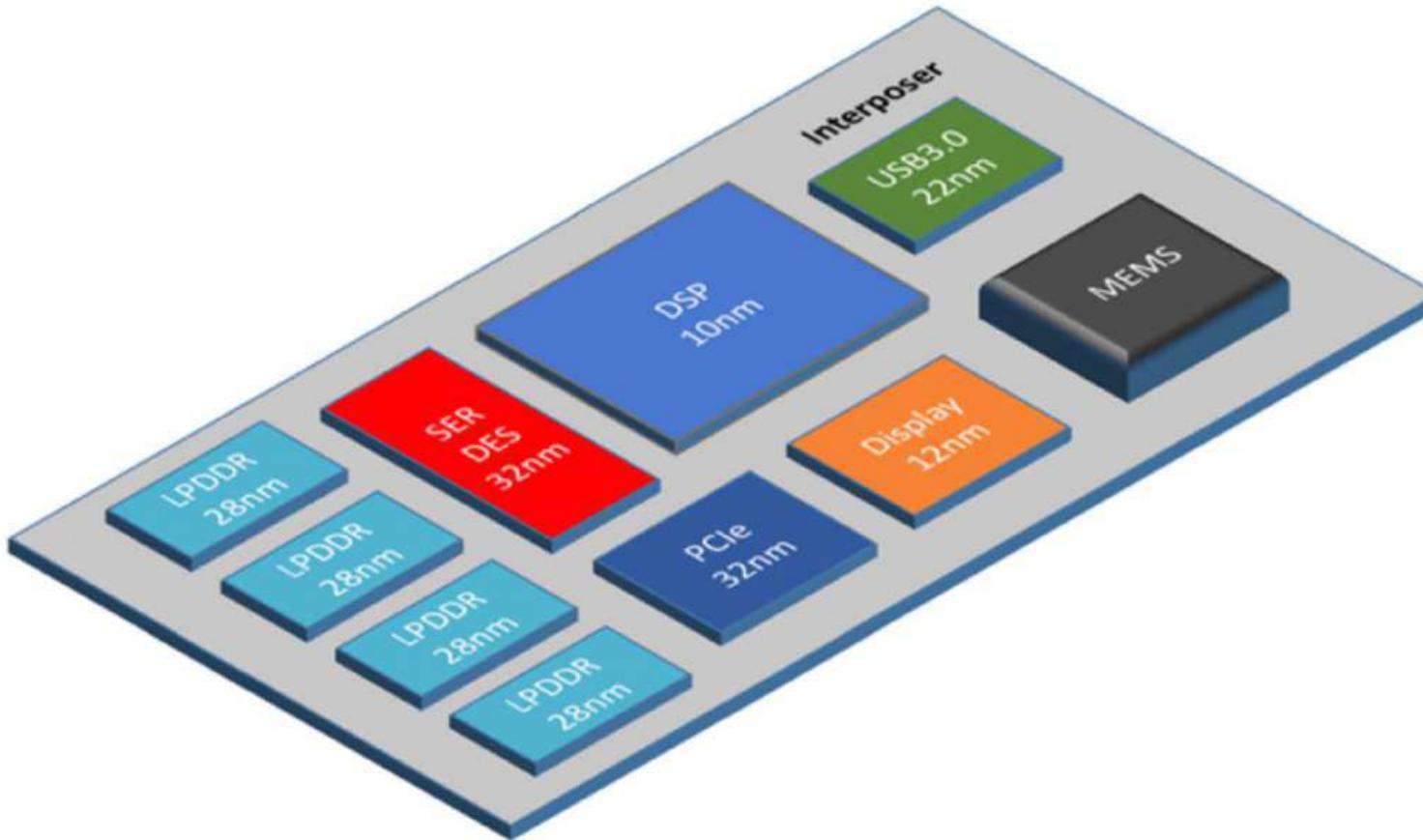




c Sustainable & Responsible Hardware Design

Opportunity: Strategically Develop
Reusable Designs and Deployments for
Full-Lifecycle Sustainability

c Sustainable & Responsible Hardware Design



A. Suleiman, Z. Zhang, L. Carlone, S. Karaman, V. Sze, Navion: A 2mW Fully Integrated Real-Time Visual-Inertial Odometry Accelerator for Autonomous Navigation of Nano Drones. JSSC. 2019.



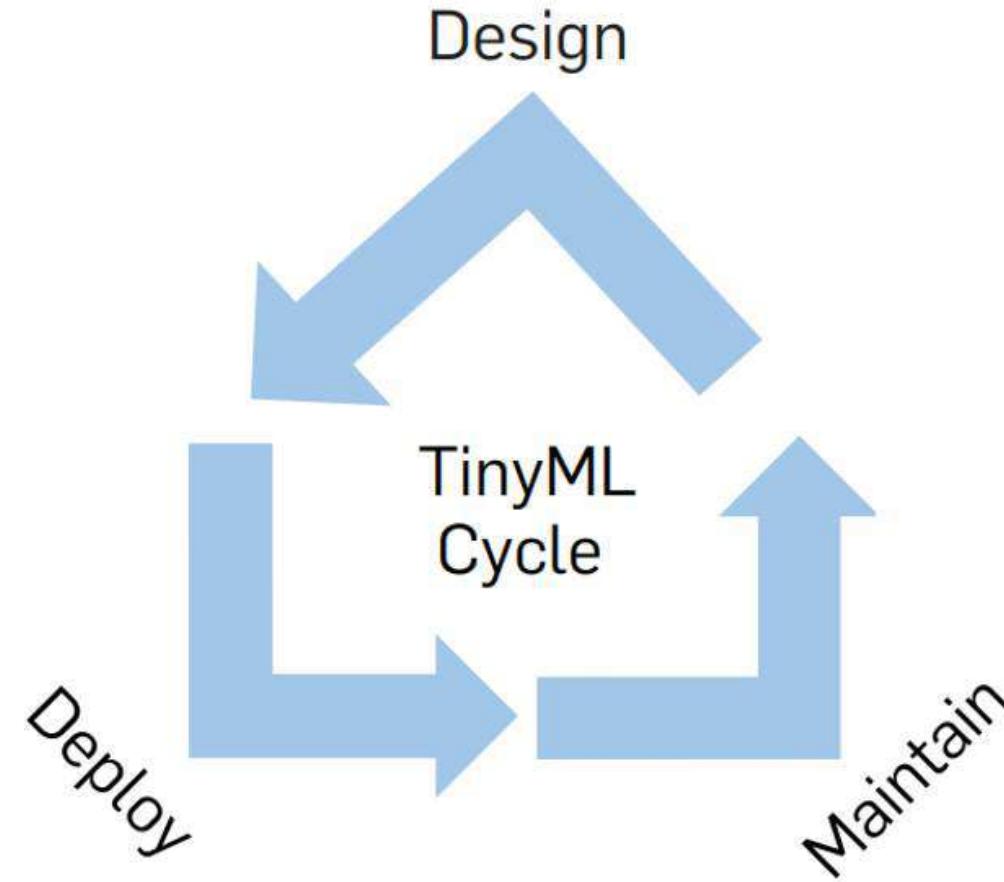
DOI:10.1145/3608473

Assessing the environmental impacts of machine learning on microcontrollers.

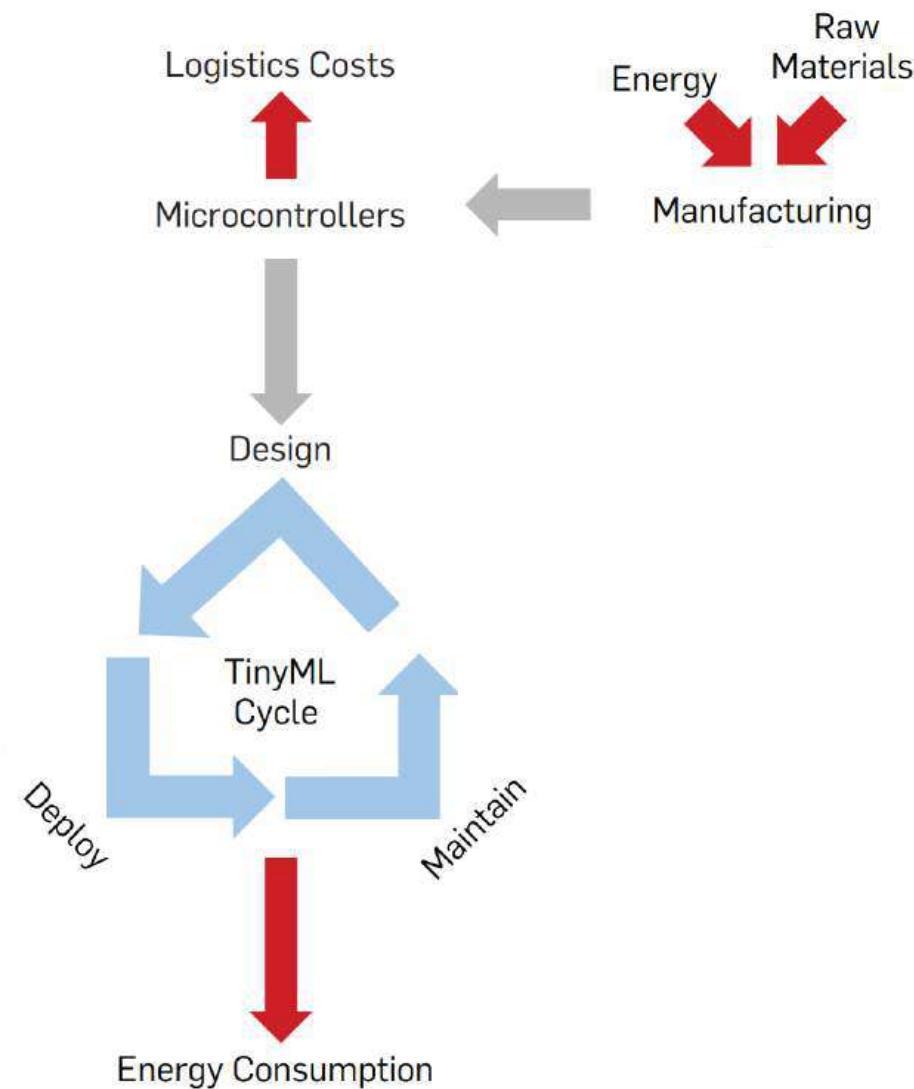
**BY SHVETANK PRAKASH, MATTHEW STEWART,
COLBY BANBURY, MARK MAZUMDER, PETE WARDEN,
BRIAN PLANCHER, AND VIJAY JANAPA REDDI**

Is TinyML Sustainable?

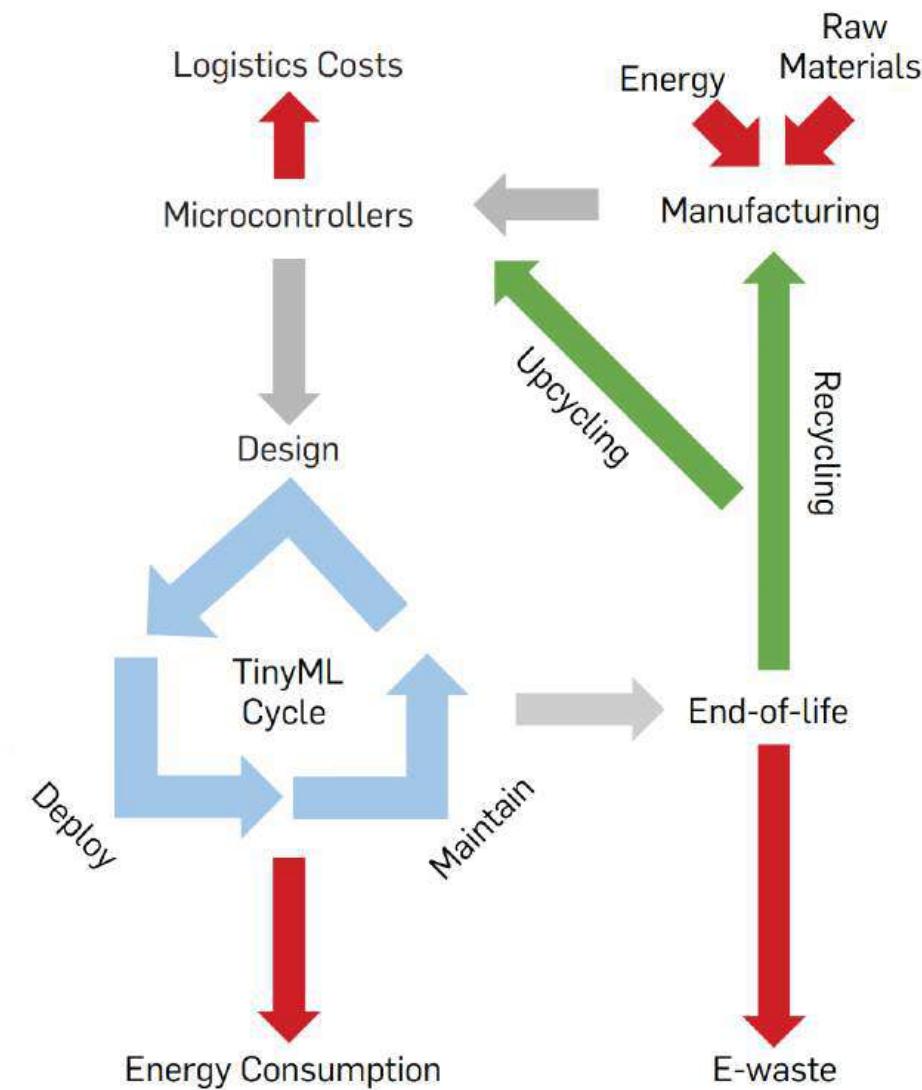
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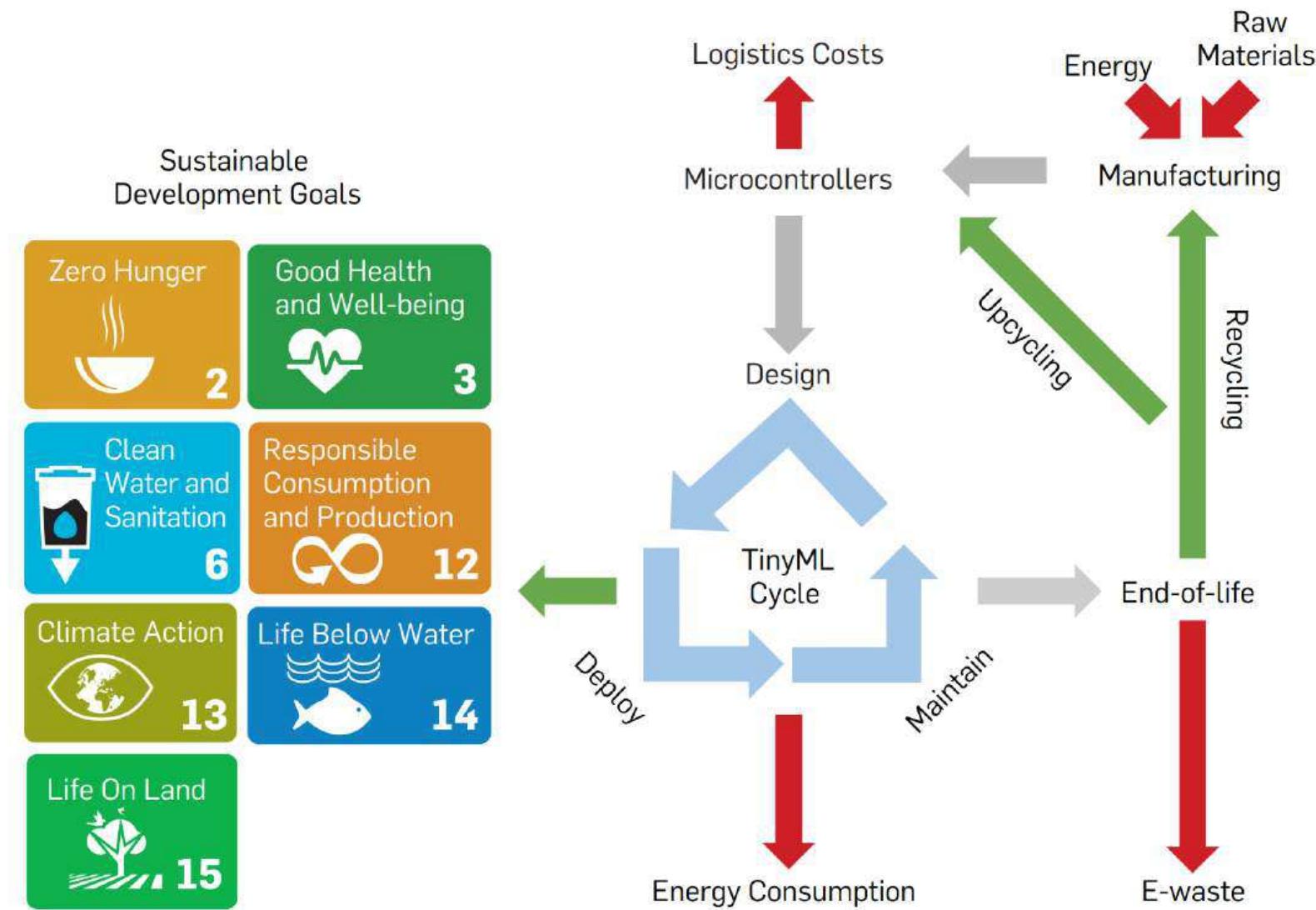
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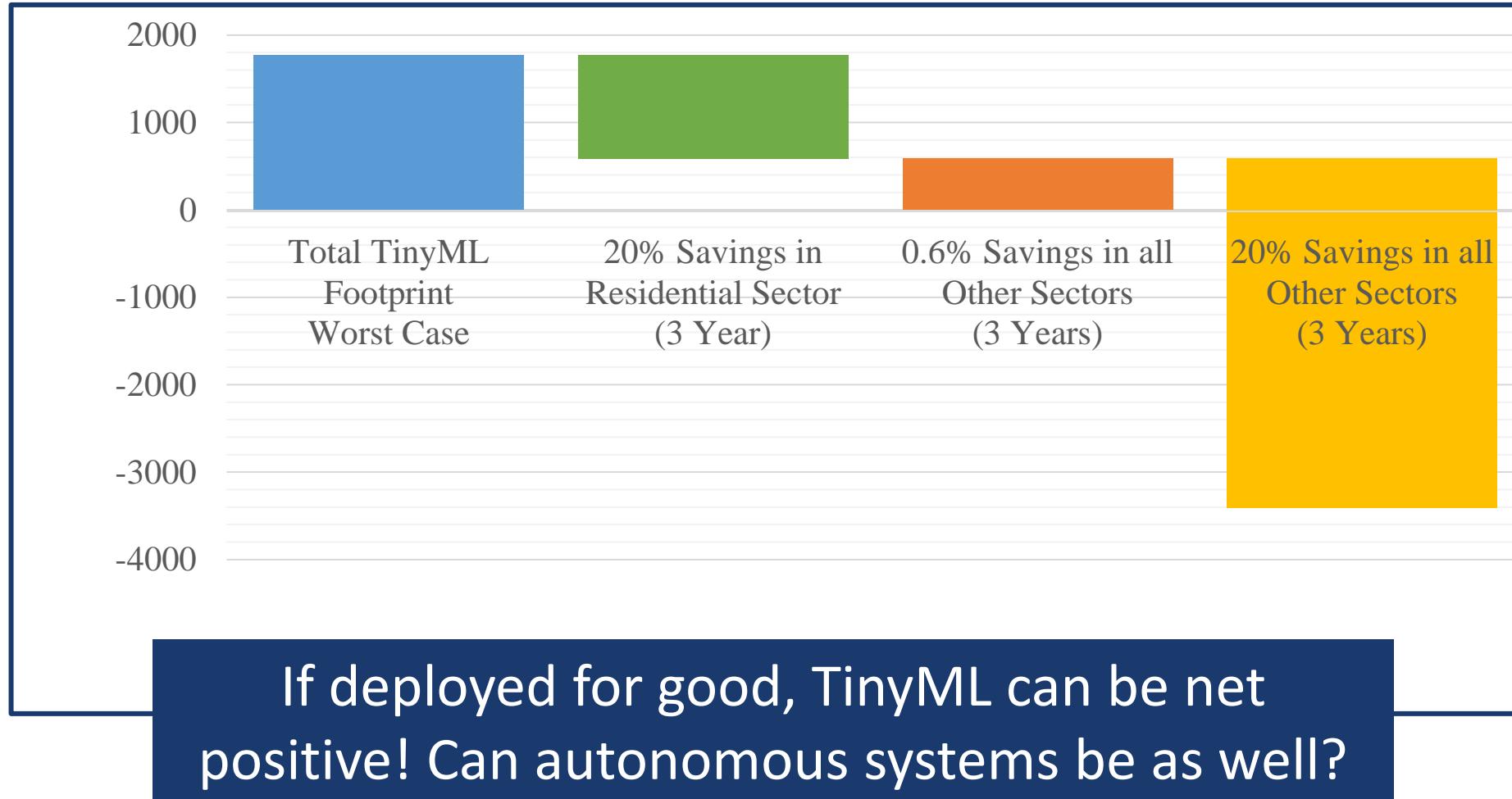
c Sustainable & Responsible Hardware Design



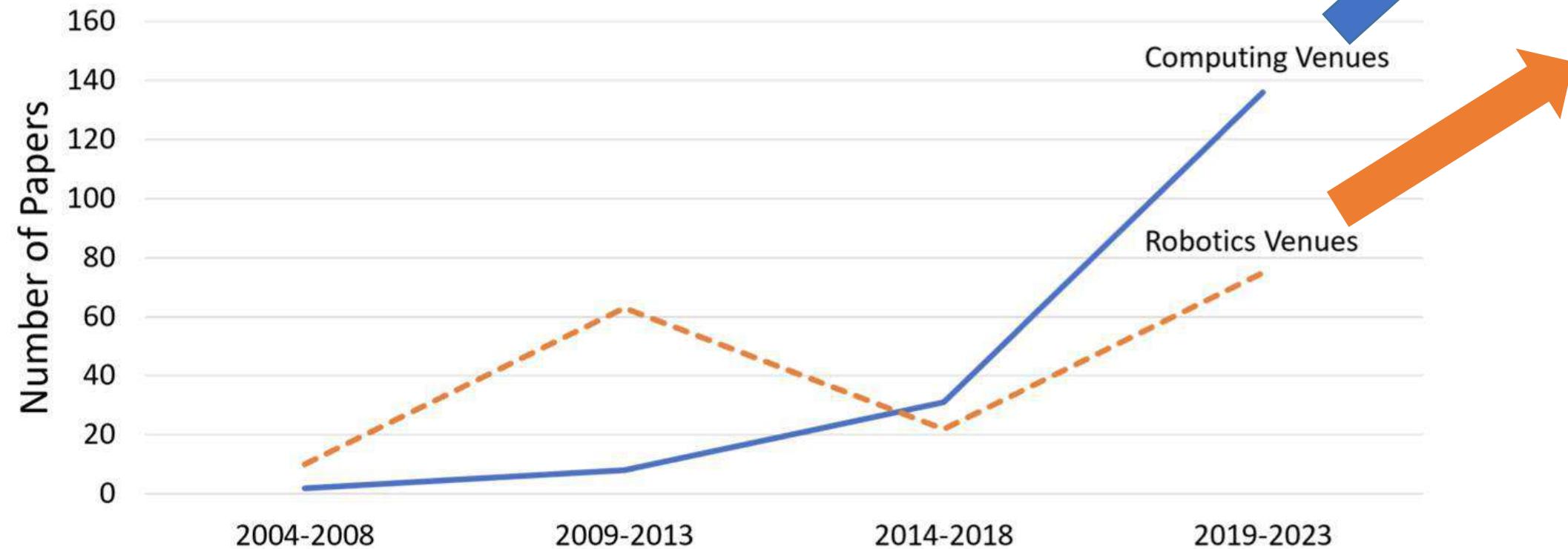
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Lets Keep Building Accelerators for Autonomous Systems!



Computing Keywords: Accelerator & Robot | Autonomous System | UAV | Drone | Autonomous Vehicle | Self-Driving, in DAC, ISCA, MICRO, HPCA, and ASPLOS. Robotics Keywords: ASIC | FPGA, in ICRA, IROS, RSS, and RA-L.

The Magnificent Seven

Challenges & Opportunities in Domain-Specific Accelerator Design for Autonomous Systems



- ① Build Bridges: Engage with Domain Experts
- ② Measure Twice, Cut Once: Metrics Matter
- ③ “Widgetism”: Avoid Over-Specialization
- ④ Pump the Brakes: Do Not Always Accelerate
- ⑤ Chips and Salsa: Acceleration Beyond ASICs
- ⑥ Forest vs. Trees: Take an End-to-End View
- ⑦ Design Global: Sustainability and Impact



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